TITLE:

DV280FBM-NB0
Product Specification

Rev. 0

Approved by	
Date	

BEIJING BOE DISPLAY TECHNOLOGY

REVISION HISTORY

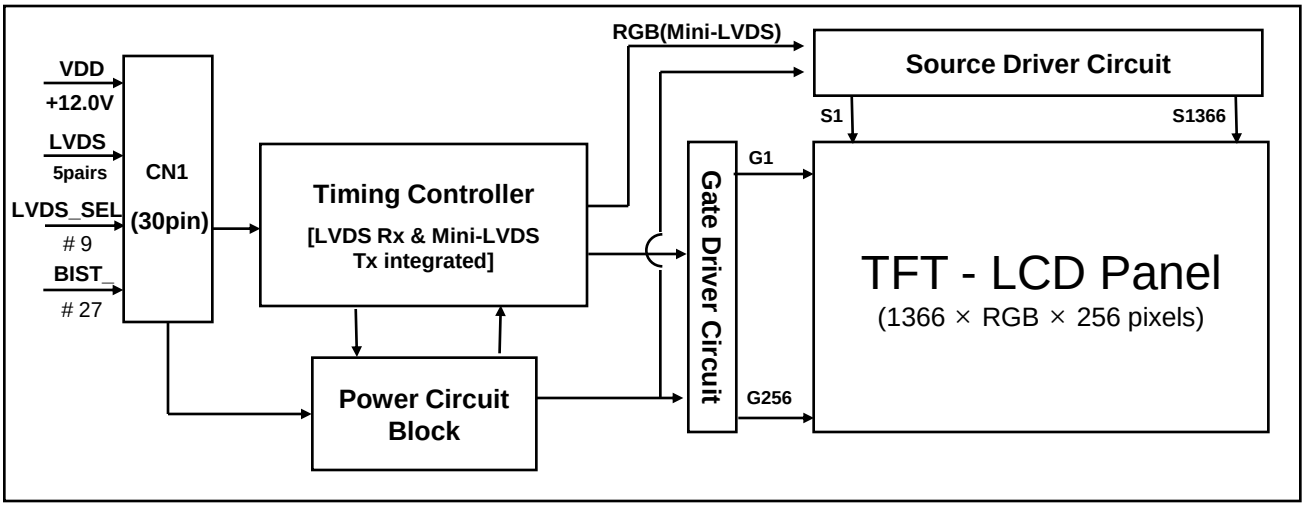
REV.	ECN NO.	DESCRIPTION OF CHANGES	DATE	PREPARED
0	-	Initial Release	2014.06.26	Song yongzhi

Contents

1.0 GENERAL DESCRIPTION

1.1 Introduction

DV280FBM-NB0 is a color active matrix TFT LCD Module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This Module has a28inch diagonally measured active area with HD resolutions (1366 horizontal by 256 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16.7M colors. The TFT-LCD panel used for this Module is adapted for a low reflection and higher color type.



1.2 Features

- LVDS interface with 1 pixel / clock
- High-speed response
- Low color shift image quality
- 8-bit color depth, display 16.7M colors
- High luminance and contrast ratio, low reflection and wide viewing angle
- DE (Data Enable) only mode
- ADS technology is applied for high display quality
- RoHS compliant

1.3 Application

- TFT-LCD Display for traffic and financial industry
- Display Terminals for diversified information
- High Definition Monitor (HD)
- AV application Products

1.4 General Specification

< Table 1. General Specifications >

Parameter	Specification	Unit	Remark
Active area	697.6845 × 130.752	mm	
Number of pixels	1366 X 256	pixels	
Pixel pitch	170.25 × 510.75	um	
Pixel arrangement	RGB vertical Strip		
Display colors	16.7M(8bits-true)	colors	
Display mode	ADS		
Outline Dimension	731.2(H) × 165.4(V) × 10.8(D)	mm	Mech
Weight	TBD	Kg	
Power Consumption	26	Watt	
Surface Treatment	Haze 25%		

2.0 ABSOLUTE MAXIMUM RATINGS

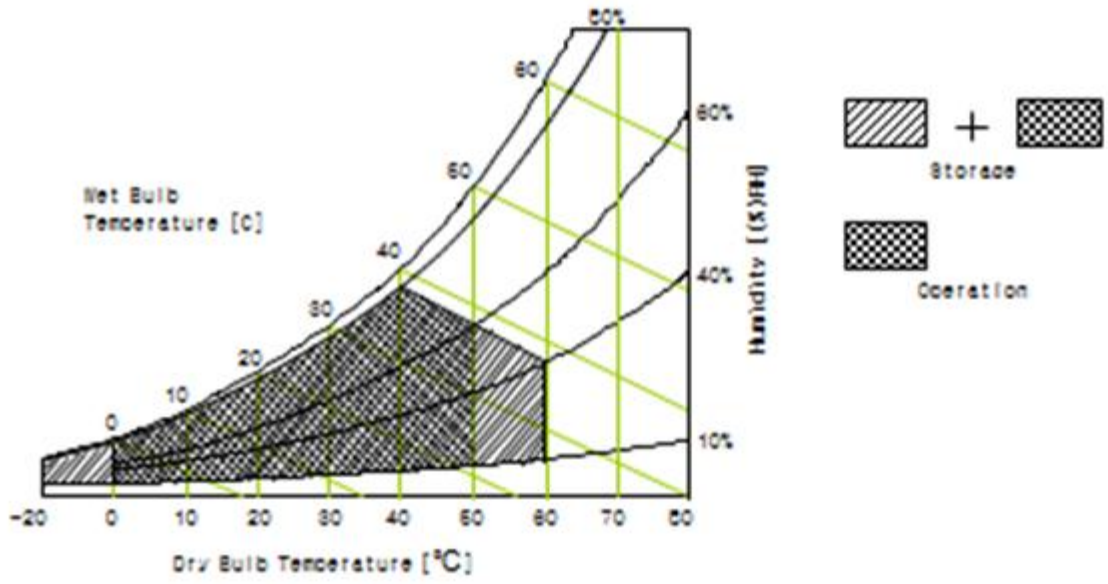
The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. LCD Open Cell Electrical Specifications >

[VSS=GND=0V]

Parameter	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	VDD	VSS-0.3	13.2	V	Ta = 25 ℃
Operating Temperature	T_{OP}	0	+50	℃	Note 1
	T_{SUR}	0	+60	℃	
Storage Temperature	T_{ST}	-20	+60	℃	
Operating Ambient Humidity	Hop	10	80	%RH	
Storage Humidity	Hst	10	80	%RH	

Note 1 : Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39 ℃ max. and no condensation of water.



3.0 ELECTRICAL SPECIFICATIONS

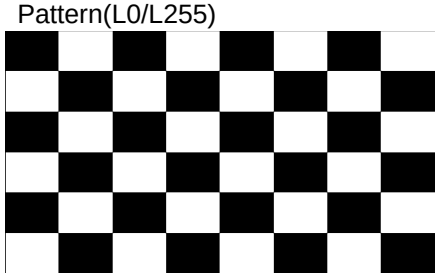
3.1 TFT LCD Open Cell

< Table 3. LCD Open Cell Electrical Specifications >

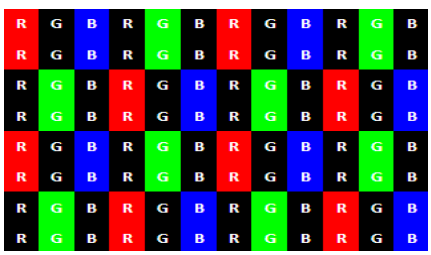
[Ta =25±2 ℃]

Parameter		Symbol	Values			Unit	Remark
			Min	Typ	Max		
Power Supply Input Voltage		VDD	10.8	12	13.2	V	
Power Supply Ripple Voltage		VRP			300	mV	
Power Supply Current		IDD	-	300	550	mA	Note 1
Power Consumption		PDD		3.6	6.6	W	
Rush current		IRUSH	-	-	3	A	Note 2
LVDS Interface	Differential Input High Threshold Voltage	VRTH	+100		+300	mV	
	Differential Input Low Threshold Voltage	VRTL	-300		-100	mV	
	Common Input Voltage	VLVC	1.0	1.2	1.4	V	
CMOS Interface	Input High Threshold Voltage	VIH	2.7	-	3.3	V	
	Input Low Threshold Voltage	VIL	0	-	0.6	V	

Note 1 : The supply voltage is measured and specified at the interface connector of LCM.
The current draw and power consumption specified is for VDD=12.0V,
Frame rate f_V=60Hz and Clock frequency = 75.4MHz.
Test Pattern of power supply current
a) Typ : Mosaic 8 x 6 Pattern(L0/L255)
Pattern(L0/L255)



b) Max : Skip 1H2V Sub Dot



Note 2 : The duration of rush current is about 2ms and rising time of Power Input is 1ms(min)

3.2 LED Converter

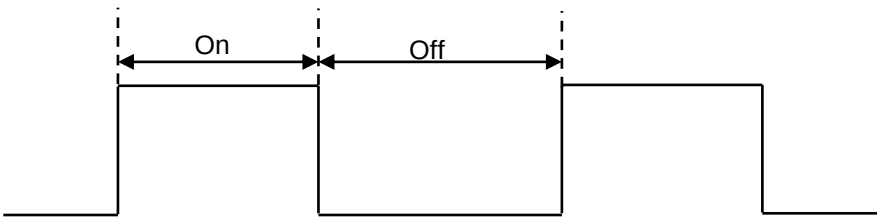
< Table 4. LED Converter Electrical Specifications >

[Ta =25±2 ℃]

Parameter	Symbol	Condition	Values			Unit	Note
			Min.	Typ.	Max.		
Input Voltage	VBL		22.8	24.0	25.2	V	
Input Current	IBL	V_{DIM}=3.3V	-	0.93	-	A	Note 1
Rush current	IRUSH	VBL= 24V	-	-	4	A	
Power Consumption	PBL	Typical Luminance	-	22.3	-	Watt	
B/L on/off control	$V_{ON/OFF}$	BL ON = High	2.4	3.3	5	V	
		BL OFF =Low	-	0	0.8	V	
PWM Frequency	F_{PWM}		120	180	200	Hz	
PWM Level	High Level		2.5	3.3	5	V	
	Low Level		-	0	0..5	V	
PWM Duty	D_{PWM}		10	-	100	%	Note 2
Life Time			30k	-	-	Hrs	Note 3

Note 1:The specified current and power consumption are under the typical supply Input voltage, 24V. It is total power consumption.

Note 2 : High-duty = On/(On+Off) * 100



Note 3 : The life time of LED, 30,000Hrs, is determined as the time at which luminance of the LED is 50% compared to that of initial value at the typical LED current on condition of continuous operating at 25 ± 2°C.

4.0 INTERFACE CONNECTION

4.1 Module Input Signal & Power

- Connector : IS100-L30B-C23(Manufactured by UJU) or Equivalent.

< Table 5. Open Cell Input Connector Pin Configuration >

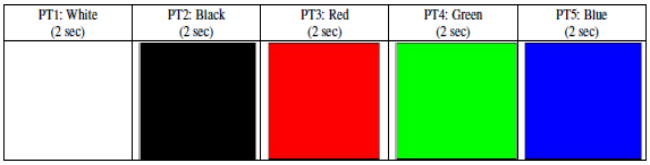
Pin No	Symbol	Description	Pin No	Symbol	Description
1	VDD	Power Supply +12.0V	16	RX1+	LVDS Receiver Signal(+)
2	VDD	Power Supply +12.0V	17	GND	Ground
3	VDD	Power Supply +12.0V	18	RX2-	LVDS Receiver Signal(-)
4	VDD	Power Supply +12.0V	19	RX2+	LVDS Receiver Signal(+)
5	GND	Ground	20	GND	Ground
6	GND	Ground	21	RCLK-	LVDS Receiver Clock Signal(-)
7	GND	Ground	22	RCLK+	LVDS Receiver Clock Signal(+)
8	GND	Ground	23	GND	Ground
9	LVDS_SEL	'L'=JEIDA , 'H'or NC= VESA	24	RX3-	LVDS Receiver Signal(-)
10	NC	No Connection	25	RX3+	LVDS Receiver Signal(+)
11	GND	Ground	26	GND	Ground
12	RX0-	LVDS Receiver Signal(-)	27	BIST	'L' or NC=Free run mode , 'H'= BIST mode
13	RX0+	LVDS Receiver Signal(+)	28	NC	No Connection
14	GND	Ground	29	NC	No Connection
15	RX1-	LVDS Receiver Signal(-)	30	GND	Ground

Notes : 1. NC(Not Connected) : This pins are only used for BOE internal operations.

2. Input Level of LVDS signal is based on the IEA 664 Standard.
3. LVDS_SEL : This pin is used for selecting LVDS signal data format.
   If this Pin : High (3.3V) or Open (NC) → Normal NS LVDS format
   Otherwise : Low (GND) → JEIDA LVDS format
4. BIST : This pin is used for selecting display pattern mode when input DE or input CLOCK quits toggling.
   If this Pin : Low (GND) or Open (NC) → Free run mode(Black Pattern)
   Otherwise : High(3.3V) → BIST mode(BIST Pattern)
   Sequence : On = VDD ≥LVDS Option , BIST Option ≥Interface signal
   Off = Interface signal ≥ LVDS Option , BIST Option ≥ VDD

Rear view of LCM

BIST Pattern



4.2 LVDS Interface

- LVDS Receiver : Timing Controller (LVDS Rx merged) / LVDS Data : Pixel Data

< Table 6. Open Cell Input Connector Pin Configuration >

	LVDS Pin	Vesa Data format	JEIDA Data format	Remark
TxOUT/RxIN0	TxIN/RxOUT0	Red0 [LSB]	R2	
	TxIN/RxOUT1	Red1	R3	
	TxIN/RxOUT2	Red2	R4	
	TxIN/RxOUT3	Red3	R5	
	TxIN/RxOUT4	Red4	R6	
	TxIN/RxOUT6	Red5	R7 [MSB]	
	TxIN/RxOUT7	Green0 [LSB]	G2	
TxOUT/RxIN1	TxIN/RxOUT8	Green1	G3	
	TxIN/RxOUT9	Green2	G4	
	TxIN/RxOUT12	Green3	G5	
	TxIN/RxOUT13	Green4	G6	
	TxIN/RxOUT14	Green5	G7 [MSB]	
	TxIN/RxOUT15	Blue0 [LSB]	B2	
	TxIN/RxOUT18	Blue1	B3	
TxOUT/RxIN2	TxIN/RxOUT19	Blue2	B4	
	TxIN/RxOUT20	Blue3	B5	
	TxIN/RxOUT21	Blue4	B6	
	TxIN/RxOUT22	Blue5	B7 [MSB]	
	TxIN/RxOUT24	HSYNC	HSYNC	
	TxIN/RxOUT25	VSYNC	VSYNC	
	TxIN/RxOUT26	DEN	DEN	
TxOUT/RxIN3	TxIN/RxOUT27	Red6	R0 [LSB]	
	TxIN/RxOUT5	Red7 [MSB]	R1	
	TxIN/RxOUT10	Green6	G0 [LSB]	
	TxIN/RxOUT11	Green7 [MSB]	G1	
	TxIN/RxOUT16	Blue6	B0 [LSB]	
	TxIN/RxOUT17	Blue7 [MSB]	B1	
	TxIN/RxOUT23	Reserved	Reserved	

4.3 LED Converter Input Signal & Power

- Connector : CI0114M1HRL-NH (Cvilux) or equivalent

< Table 7. LED Converter Input Connector Pin Configuration >

Pin No	Symbol	Description	Remarks
1	VBL	Power Supply +24V	
2	VBL	Power Supply +24V	
3	VBL	Power Supply +24V	
4	VBL	Power Supply +24V	
5	VBL	Power Supply +24V	
6	GND	Ground and Current Return	
7	GND	Ground and Current Return	
8	GND	Ground and Current Return	
9	GND	Ground and Current Return	
10	GND	Ground and Current Return	
11	DET	Normal (Low) / Abnormal (Open Collector)	Low : 0~0.8V
12	VBLON/OFF	Backlight ON/OFF control	On : 2.4V~5.0V/Off :0~0.8V
13	NC	No Connection	
14	PDIM	External PWM control signal	

Notice: While system is turned ON or OFF, the power sequences must follow as below descriptions:
Turn ON sequence: VBL → PWM signal → BLON
Turn OFF sequence: BLOFF → PWM signal → VBL

5.0 SIGNAL TIMING SPECIFICATION

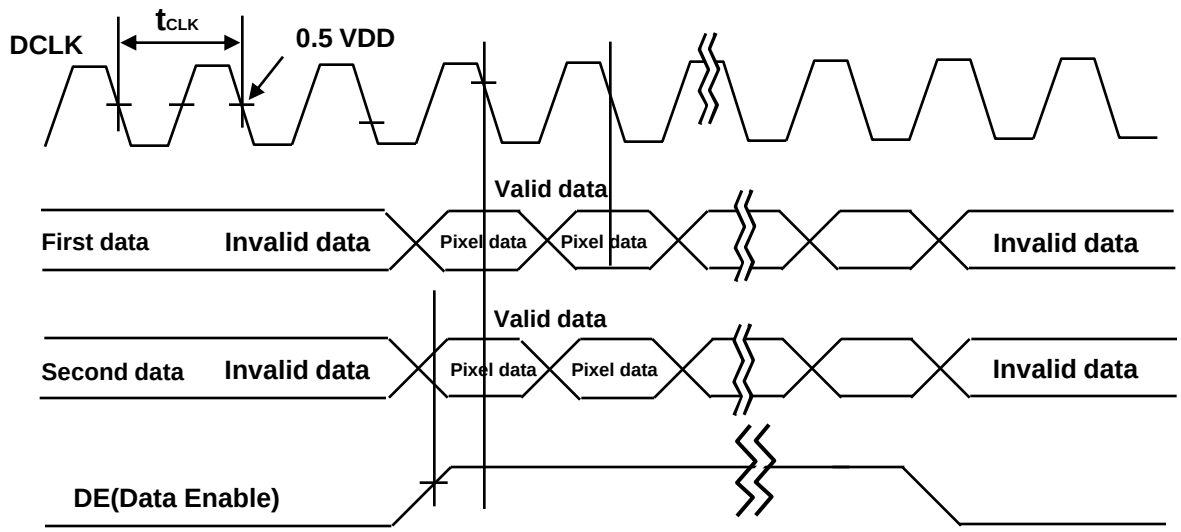
5.1 Timing Parameters (DE only mode)

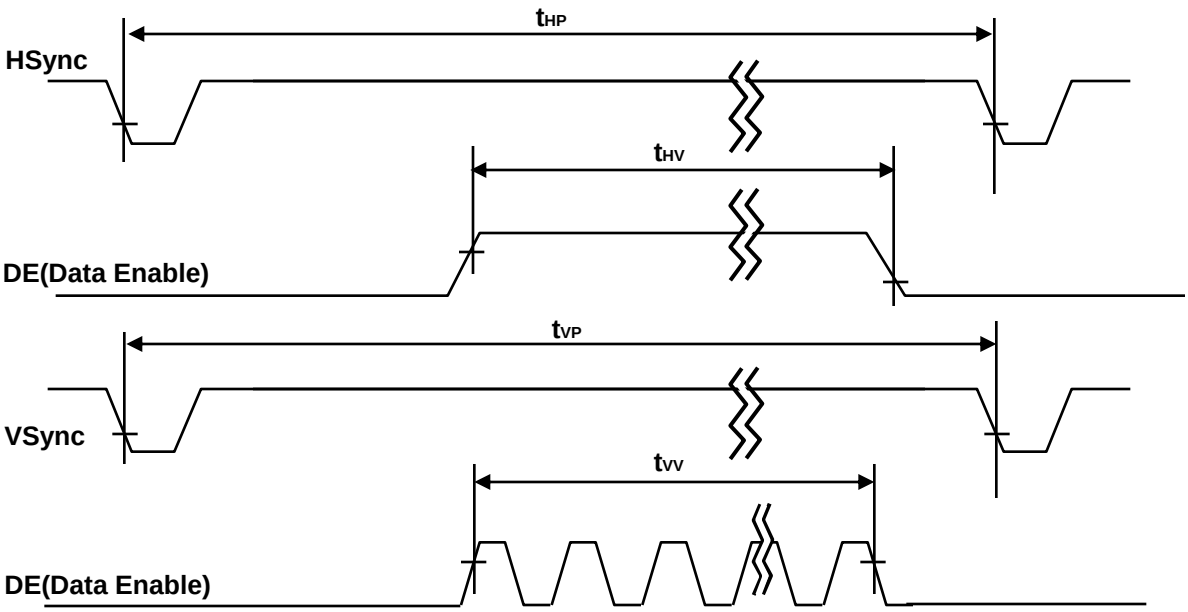
< Table 8. Timing Table >

ITEM	Symbol		Min	Typ	Max	Unit	Note
CLK	Period	t_{CLK}	11.8	13.3	17.9	ns	
	Frequency	-	56	75.4	85.0	MHz	
Hsync	Period	t_{HP}	1450	1560	2000	t_{CLK}	
	Frequency	f_H	39.4	48.4	55	KHz	
Vsync	Period	t_{VP}	778	806	1200	t_{HP}	
	Frequency	f_V	47	60	65	Hz	
Horizontal Active Display Term	Valid	t_{HV}	-	1366	-	t_{CLK}	
	Total	t_{HP}	1450	1560	2000	t_{CLK}	
Vertical Active Display Term	Valid	t_{VV}	-	768	-	t_{HP}	
	Total	t_{VP}	778	806	1200	t_{HP}	

Notes: This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

5.2 Signal Timing Waveform



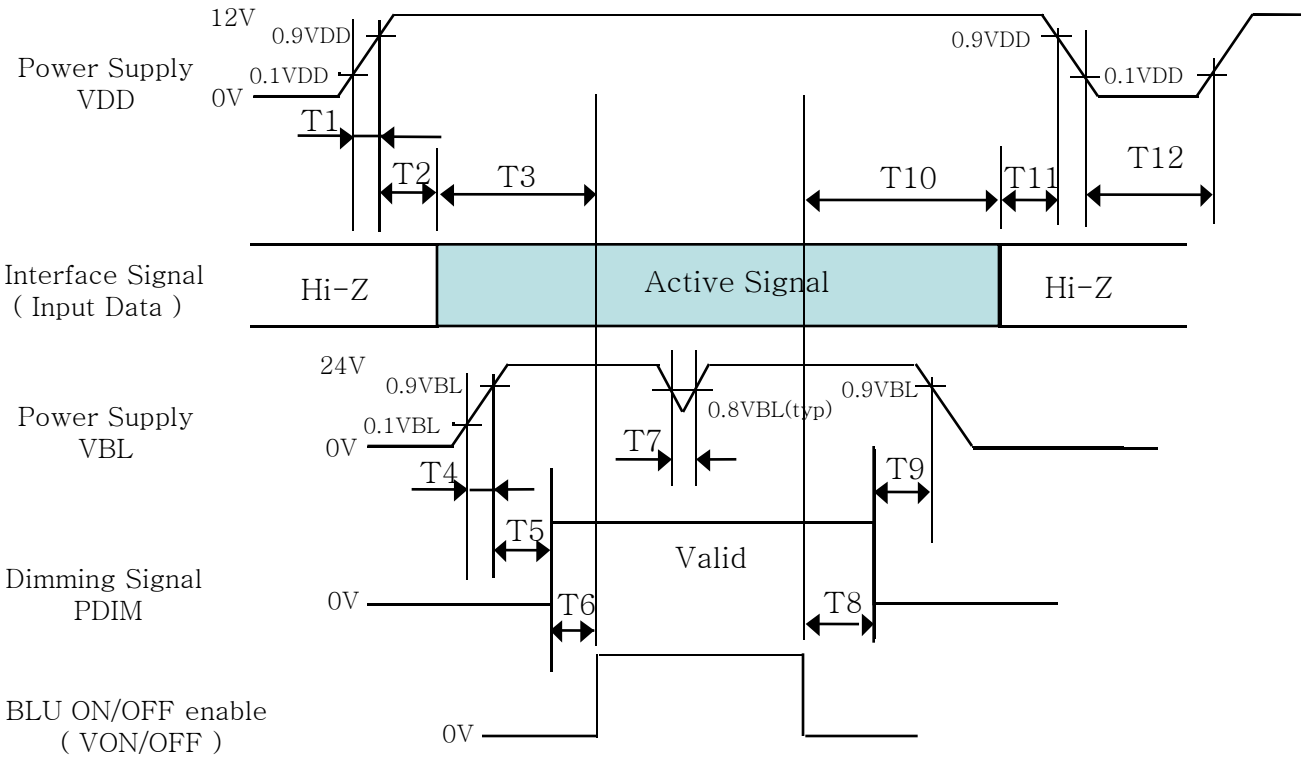


5.3 Input Signals, Basic Display Colors and Gray Scale of Colors

< Table 9. Input Signal and Display Color Table >

Color & Gray Scale		Input Data Signal																							
		Red Data								Green Data								Blue Data							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	▽	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	▽	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale of Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	▽	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Gray Scale of White	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
	▽	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

5.4 Power Sequence



< Table 10. Sequence Table >

Parameter	Values			Units
	Min	Typ	Max	
T1	0.5	-	10	ms
T2	0	-	50	ms
T3	200	-	-	ms
T4	20	-	-	ms
T5	100	-	-	ms
T6	0	-	-	ms
T7	-	-	10	ms
T8	0	-	-	ms
T9	100	-	-	ms
T10	200	-	-	ms
T11	0	-	50	ms
T12	1	-	-	s

Notes: Back Light should be turned on after power for logic and interface signal are valid.

6.0 OPTICAL SPECIFICATIONS

The test of optical specifications shall be measured in a dark room (ambient luminance≤1 lux and temperature=25±2℃) with the equipment of Luminance meter system (Goniometer system and PR730) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0°. We refer to $\theta_{Ø=0}$ (=θ_3) as the 3 o'clock direction (the "right"), $\theta_{Ø=90}$ (= θ_{12}) as the 12 o'clock direction ("upward"), $\theta_{Ø=180}$ (= θ_9) as the 9 o'clock direction ("left") and $\theta_{Ø=270}$(= θ_6) as the 6 o'clock direction ("bottom"). While scanning θ and/or Ø, the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 12.0V +/-10% at 25°C. Optimum viewing angle direction is 6 'clock.

< Table 11. Optical Table >

[VDD = 12.0V, Frame rate = 120Hz, Ta =25±2 ℃]

Parameter			Symbol	Condition	Min	Typ	Max	Unit	Remark
Viewing Angle	2D	Horizontal	Θ_3	CR > 10		89		Deg.	Note 1
			Θ_9			89		Deg.	
		Vertical	Θ_{12}			89		Deg.	
			Θ_6			89		Deg.	
Contrast ratio			CR	Θ = 0° (Center) Normal Viewing Angle		1200:1			Note 2
Luminance of White			Y_w			800		cd/m^2	Note3
White luminance uniformity			ΔY			75		%	Note4
Color Gamut						68		%	
Reproduction of color	White		W_x			0.280			Note5
			W_y			0.290			
	Red		R_x			TBD.			
			R_y			TBD.			
	Green		G_x			TBD.			
			G_y			TBD.			
	Blue		B_x			TBD.			
			B_y			TBD.			
Response Time	G to G		T_g		6	8	10	ms	Note 6
Gamma Scale					2.0	2.2	2.4		
Cell Transmittance						6.7		%	

Note :

1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.
2. Contrast measurements shall be made at viewing angle of $\theta = 0°$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See Figure 1 shown in Appendix) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. Center Luminance of white is defined as the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in Figure 2 for a total of the measurements per display.
4. The White luminance uniformity on LCD surface is then expressed as :
   ΔY = (Minimum Luminance of 5 points / Maximum Luminance of 5 points) * 100
   (See Figure 2 shown in Appendix).
5. The color chromaticity coordinates specified in Table 11. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
6. Response time Tg is the average time required for display transition by switching the input signal as below table and is based on Frame rate fV =60Hz to optimize.
   Each time in below table is defined as Figure 3 and shall be measured by switching the

Measured Response Time		Target																
		0	15	31	47	63	79	95	111	127	143	159	175	191	207	223	239	255
Start	0																	
	15																	
	31																	
	47																	
	63																	
	79																	
	95																	
	111																	
	127																	
	143																	
	159																	
	175																	
	191																	
	207																	
	223																	
	239																	
	255																	

7.0 MECHANICAL CHARACTERISTICS

7.1 Dimensional Requirements

Figure 4(located in Appendix) shows mechanical outlines for the model DVB280FBM-NB0
Other parameters are shown in Table 11.

< Table 12. Dimensional Parameters >

Parameter	Specification	Unit
Dimensional outline	731.2(H) × 165.4(V) × 10.8(D)	mm
Weight	TBD	Kg
Active area	697.7(H)×130.8(V)	mm
Pixel pitch	170.25 × 510.75	um
Number of pixels	1366(H) ×256(V) (1 pixel = R + G + B dots)	pixels
Back-light	Edge Type LED Backlight	

7.2 Mounting

See Figure 5 (Shown in Appendix)

7.3 Anti-Reflection and Polarizer Hardness

The surface of the LCD has an Anti-Reflection coating to minimize reflection and a coating to reduce scratching.

8.0 Reliability Test Condition

< Table 13. Reliability Test Condition >

Item	Test Condition
High-Temp/STG	Ta = 60 ℃, 240 hrs
Low-Temp/STG	Ta = -20 ℃, 240 hrs
High-Temp/HMD	Ta = 50 ℃, 80%RH, 240hrs
High-Temp/OP	Ta = 50 ℃, 240hrs
Low-Temp/OP	Ta = 0 ℃, 240hrs
TST	Ta = -20 ℃ ↔ 60 ℃ (0.5 hr), 100 cycle
Vibration	Frequency:10-300 Hz Gravity / AMP : 1.0 G rms Period : X, Y, Z 30 min
Shock	Gravity : 50G Pulse width : 11msec, Half Sine ±X, ±Y, ±Z Once for each direction
ESD	Air: ±15kV,150pF/330Ω,100Point,1time/Point Contact: ±8kV,150pF/330Ω,100Point, 1time/Point

This test condition is based on BOE module.

9.0 PRODCUT SERIAL NUMBER

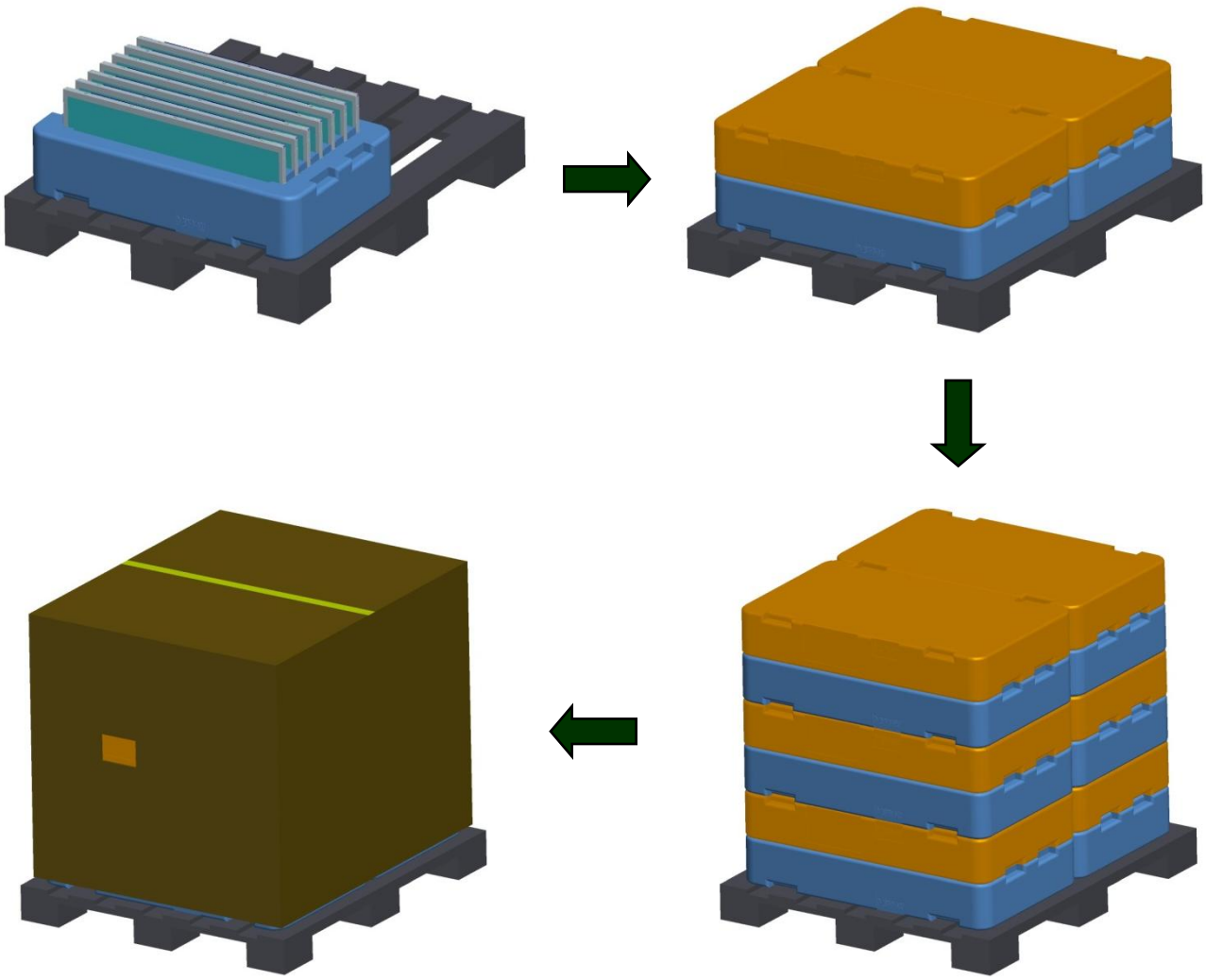
1		2	3	4		5	6				7						
X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X

1. Control Number
2. Rank / Grade
3. Line Classification
4. Year (2011 : 11, 2012 : 12, …)
5. Month (1,2,3, … , 9, X, Y, Z)
6. Internal Use
7. Serial Number

10.0 PACKING INFORMATION

BOE provides the standard shipping container for customers, unless customer specifies their packing information. The standard packing method and Barcode information are shown in below.

10.1 Packing Order



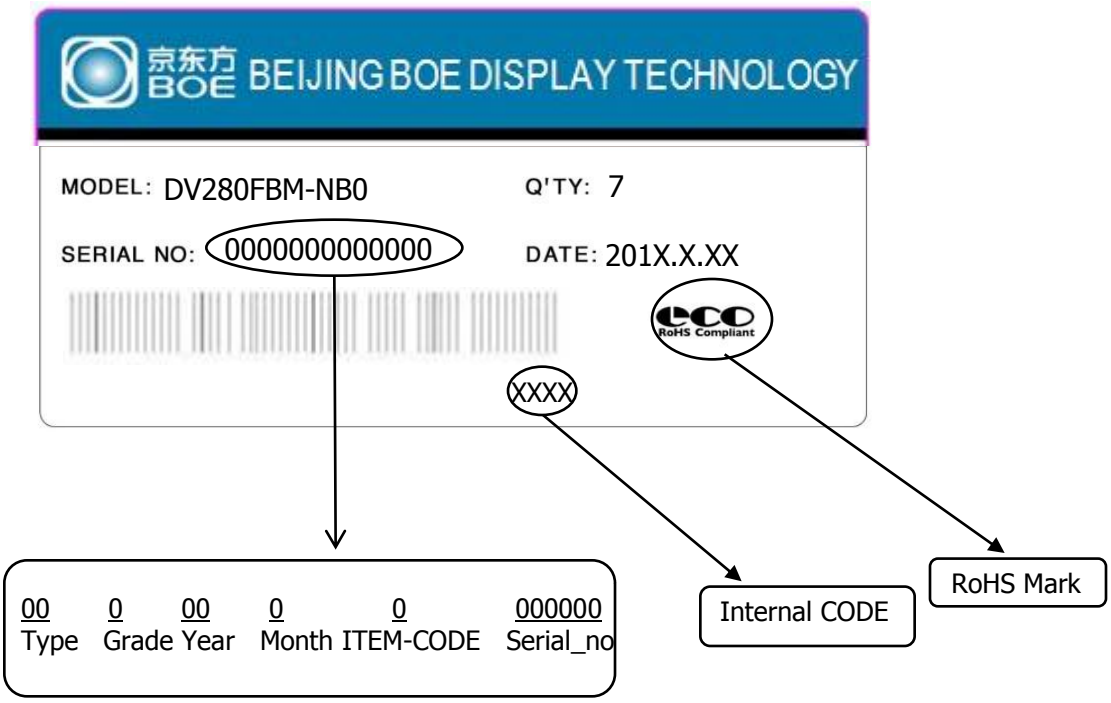
10.2 Packing Note

- Box Dimension : 890mm(L) × 530mm (W) ×297.4mm (H)
- Package Quantity in one Box : 7pcs

10.3 Box Label

- Label Size : 110 mm (L) × 55 mm (W)
- Contents

  Model : DV280FBM-NB0
  Q`ty : 7pcs Module in one box.
  Serial No. : Box Serial No. See next page for detail description.
  Date : Packing Date



11.0 HANDLING & CAUTIONS

(1) Cautions when taking out the module
- Pick the pouch only, when taking out module from a shipping package.

(2) Cautions for handling the module
- As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
- As the LCD panel and back - light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
- As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
- Do not pull the interface connector in or out while the LCD module is operating.
- Put the module display side down on a flat horizontal plane.
- Handle connectors and cables with care.

(3) Cautions for the operation
- When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
- Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.

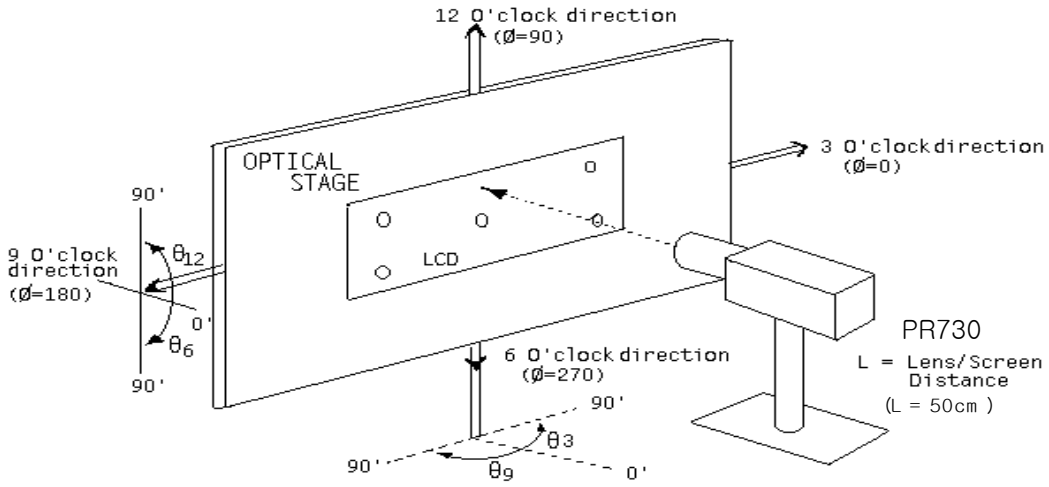
(4) Cautions for the atmosphere
- Dew drop atmosphere should be avoided.
- Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.

(5) Cautions for the module characteristics
- Do not apply fixed pattern data signal to the LCD module at product aging.
- Applying fixed pattern for a long time may cause image sticking.

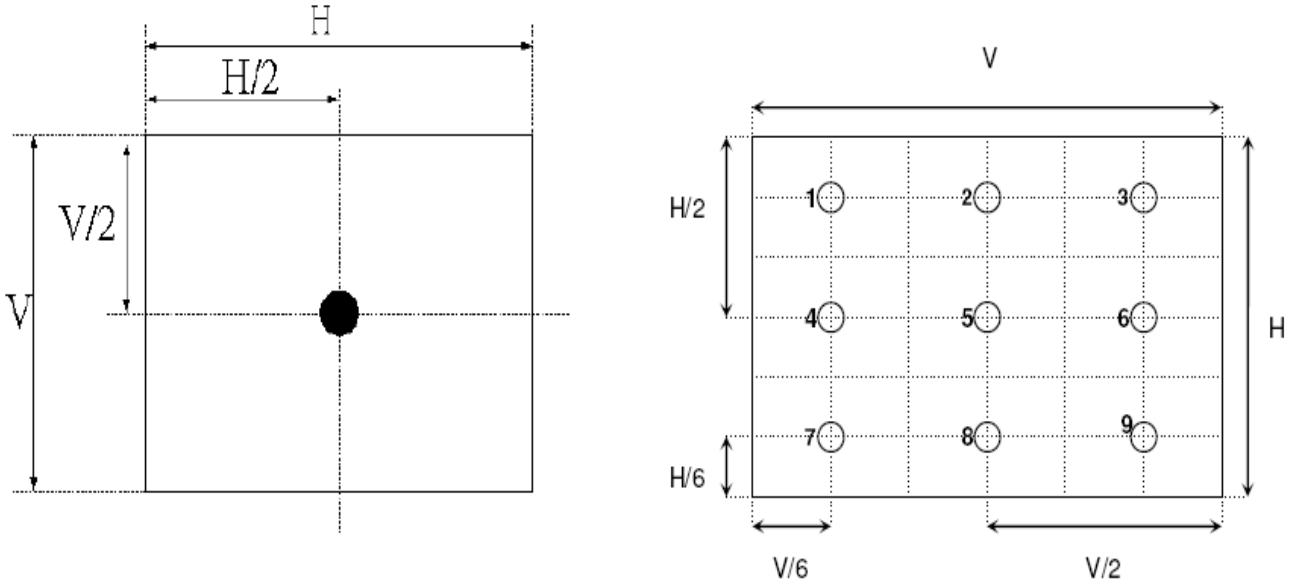
(6) Other cautions
- Do not disassemble and/or re-assemble LCD module.
- Do not re-adjust variable resistor or switch etc.
- When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

12.0 APPENDIX

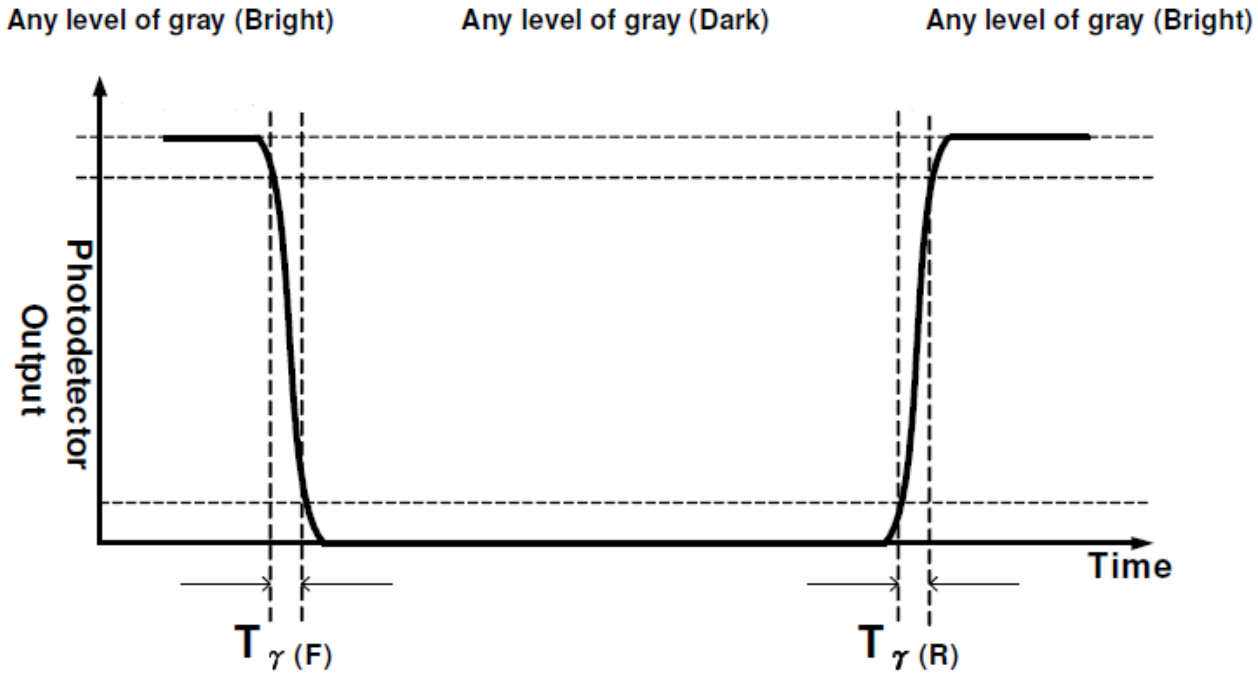
< Figure 1. Measurement Set Up >



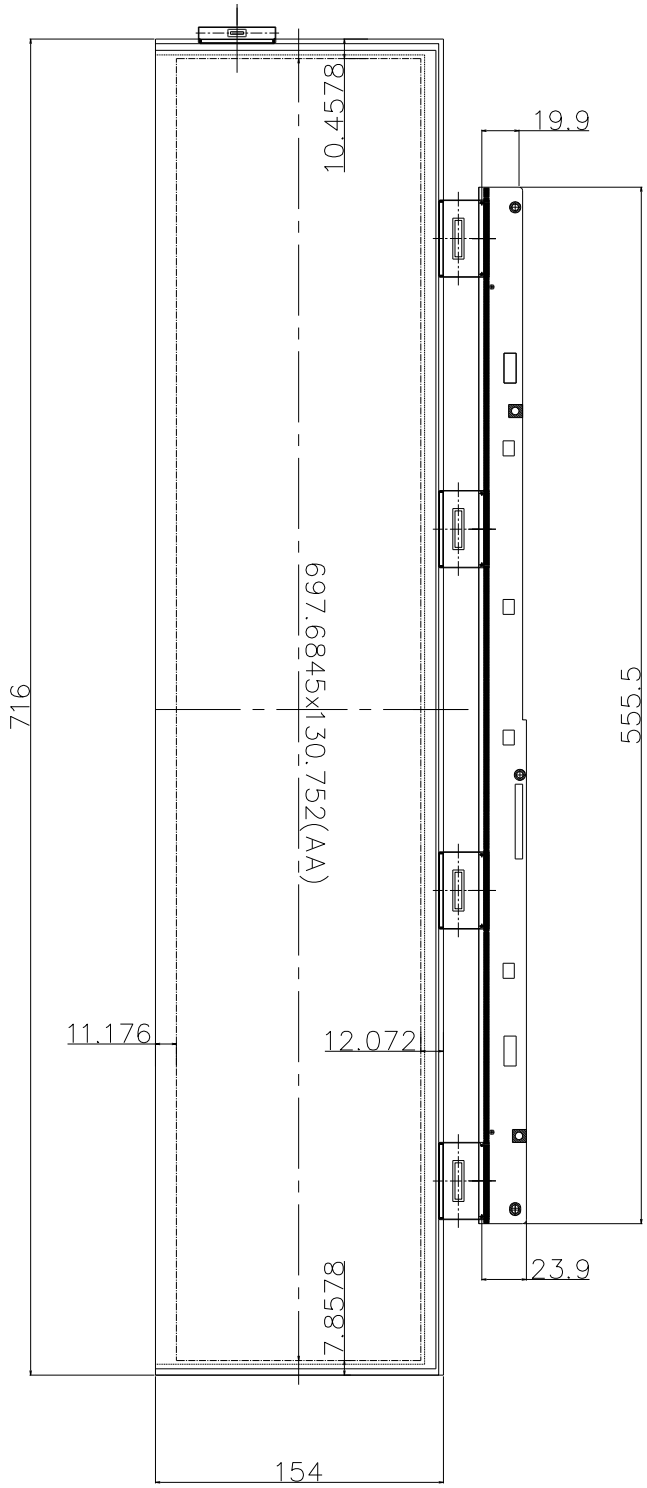
< Figure 2. White Luminance and Uniformity Measurement Locations >



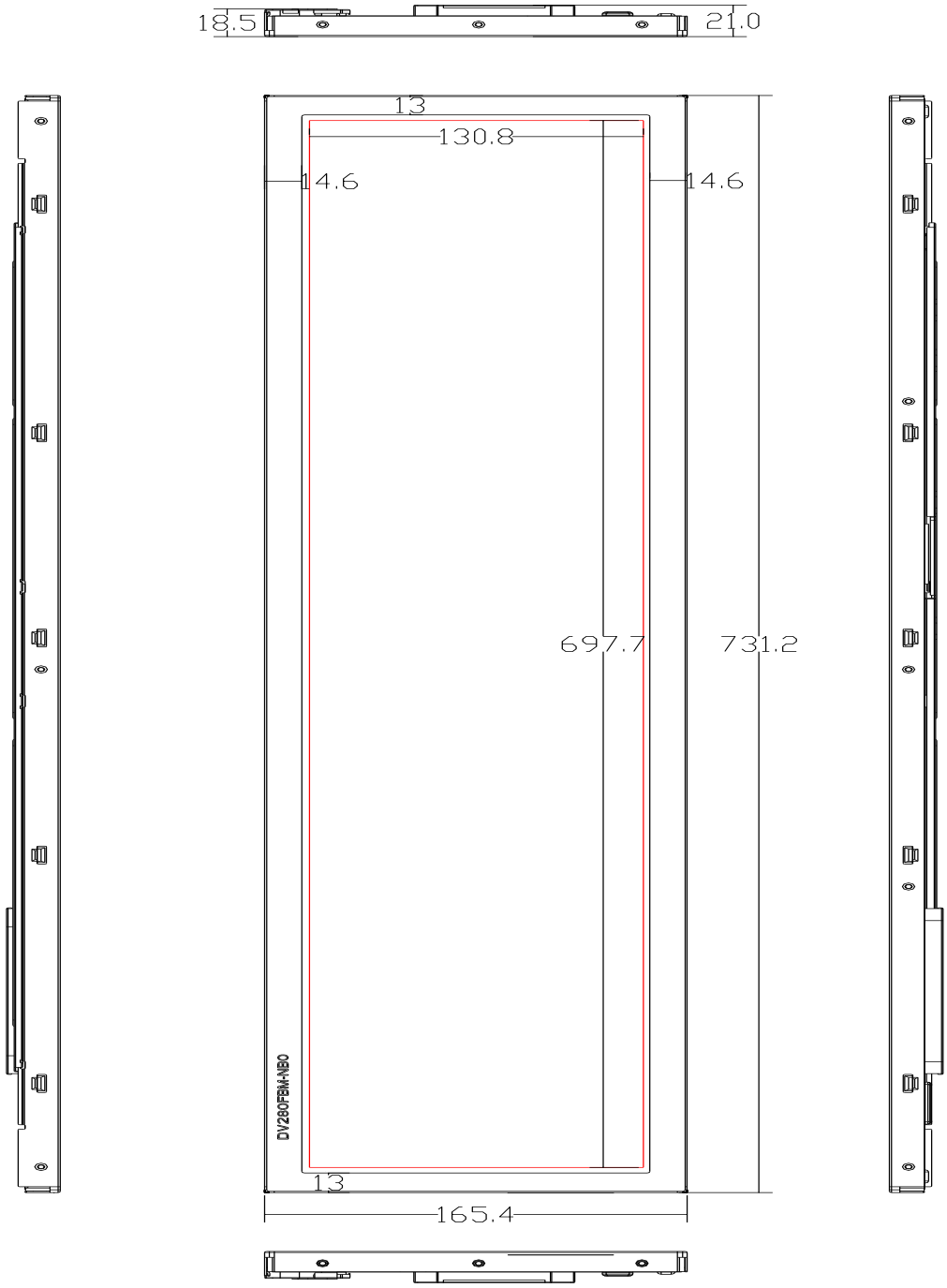
< Figure 2. Response Time Testing >



< Figure 3. TFT-LCD Open Cell (Front View) >



< Figure 4. TFT-LCD Module Outline Dimensions (Front View) >



< Figure 5. TFT-LCD Module Outline Dimensions (Rear View) >