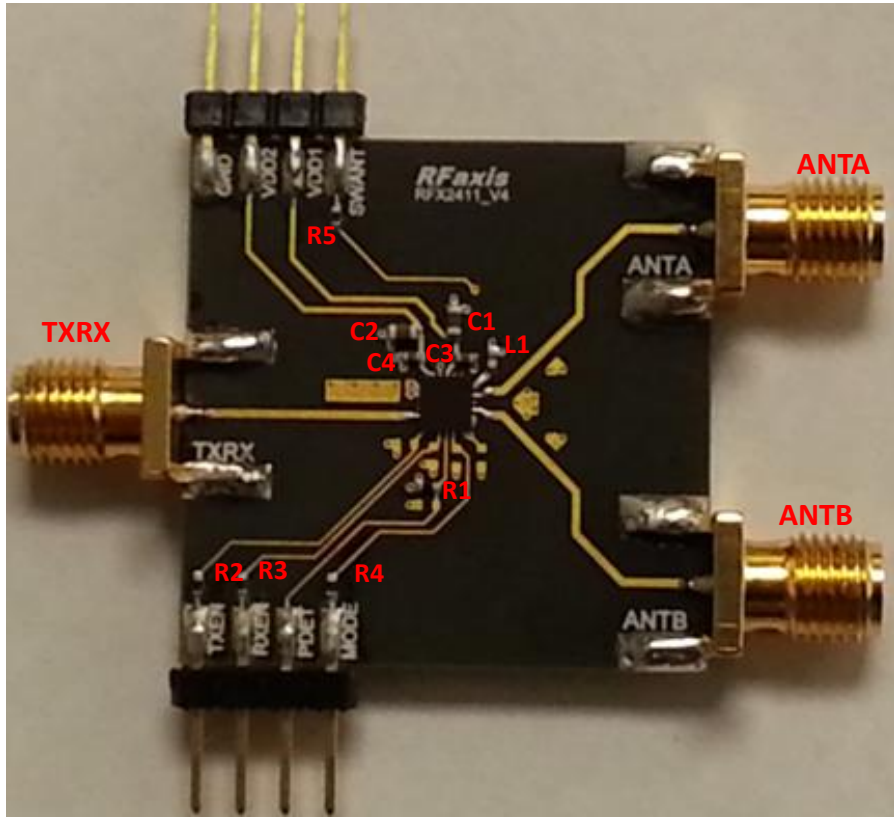


GND
VDD2
VDD1
SWant



TXEN
RXEN
PDET
Mode

SWant	Mode of Operation
1	ANTA port enabled
0	ANTB port enabled

For VDD decoupling:
C1, C2=2.2uF
C3, C4=220pF

Detector Loading:
R1 = 10Kohm

Digital Control Protection:
R2 = R3 = R4 = R5 = 1Kohm (Recommended for control lines with voltage that may approach Vdd levels).

For Harmonic Suppression:
L1=1.2nH (Inductor value may need to be optimized in final application circuit since it is layout dependent)
Additional filtering may be required for compliance depending on system configuration and application.

Eval PCB Information:

- 4-Layer Stack, 10mil/40mil/10mil
- FR4 with $\epsilon_r=4.5$, $\tan \delta = 0.02$ (typ.)
- TXRX, ANTA, ANTB Trace Loss ~ 0.22 dB.
- All trace losses have been de-embedded from the following Measurements.

Control Logic Truth Table

TXEN	RXEN	MODE	Mode of Operation
0	0	0	Shutdown Mode
0	0	1	Bypass Mode (Bi-directional)
1	X	0	Transmit Mode
0	1	0	Low Noise Figure Receive Mode
0	1	1	Low Current Receive Mode

Note: "1" denotes high voltage state (> 1.2V) at Control Pins
"0" denotes low voltage state (< 0.3V) at Control Pins