



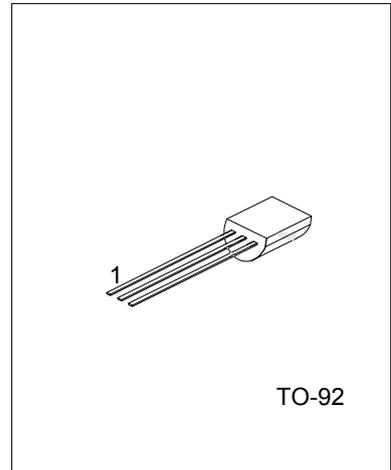
MPSA92M

PNP EPITAXIAL SILICON TRANSISTOR

HIGH VOLTAGE TRANSISTOR

FEATURES

- * Collector-Emitter voltage: $V_{CE0} = -300V$
- * Collector Dissipation: $P_{C(MAX)} = 625mW$
- * Low collector-Emitter saturation voltage



ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
MPSA92ML-T92-B	MPSA92MG-T92-B	TO-92	E	B	C	Tape Box
MPSA92ML-T92-K	MPSA92MG-T92-K	TO-92	E	B	C	Bulk
MPSA92ML-T92-R	MPSA92MG-T92-R	TO-92	E	B	C	Tape Reel

Note: Pin Assignment: E:EMITTER B:BASE C:COLLECTOR

<p>MPSA92ML-T92-B</p> <p>(1) Packing Type (2) Package Type (3) Lead Free</p>	<p>(1) B: Tape Box, K: Bulk, R: Tape Reel (2) T92: TO-92 (3) Halogen Free, L: Lead Free</p>
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■ ABSOLUTE MAXIMUM RATINGS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Base Voltage	V_{CBO}	-300	V
Collector-Emitter Voltage	V_{CEO}	-300	V
Emitter-Base Voltage	V_{EBO}	-6	V
Collector Dissipation ($T_A=25^\circ\text{C}$)	P_C	625	mW
Collector Current	I_C	-800	mA
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55~+150	$^\circ\text{C}$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	BV_{CBO}	$I_C=-100\mu\text{A}$, $I_E=0$	-300			V
Collector-Emitter Breakdown Voltage	BV_{CEO}	$I_C=-1\text{mA}$, $I_B=0$	-300			V
Collector-Emitter Breakdown Voltage	BV_{CES}	$I_C=-100\mu\text{A}$, $V_{BE}=0$	-300			V
Emitter-Base Breakdown Voltage	BV_{EBO}	$I_E=-100\mu\text{A}$, $I_C=0$	-5			V
Collector Cut-Off Current	I_{CBO}	$V_{CB}=-300\text{V}$, $I_E=0$			-100	nA
Collector Cut-Off Current	I_{CES}	$V_{CB}=-300\text{V}$, $V_{BE}=0$			-1	μA
Emitter Cut-Off Current	I_{EBO}	$V_{EB}=-4\text{V}$, $I_C=0$			100	nA
DC Current Gain (Note 1)	h_{FE}	$V_{CE}=-10\text{V}$, $I_C=-1\text{mA}$	60			
		$V_{CE}=-10\text{V}$, $I_C=-10\text{mA}$	80		300	
		$V_{CE}=-10\text{V}$, $I_C=-100\text{mA}$	80			
		$V_{CE}=-10\text{V}$, $I_C=-200\text{mA}$	40			
Collector-Emitter Saturation Voltage	$V_{CE(SAT)}$	$I_C=-30\text{mA}$, $I_B=-1\text{mA}$			-0.20	V
		$I_C=-100\text{mA}$, $I_B=-10\text{mA}$			-0.7	
Base-Emitter Saturation Voltage	$V_{BE(SAT)}$	$I_C=-10\text{mA}$, $I_B=-1\text{mA}$			-0.75	V
Output Capacitance	C_{OB}	$V_{CB}=-20\text{V}$, $I_E=0$, $f=1\text{MHz}$			8	pF

Note: 1. Pulse test: $PW<300\mu\text{s}$, Duty Cycle $<2\%$

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