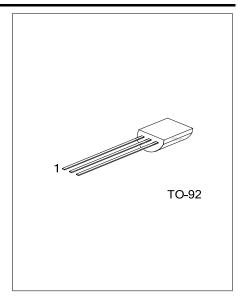
03N70-KW Preliminary Power MOSFET

0.3A, 700V N-CHANNEL POWER MOSFET

■ DESCRIPTION

The UTC **03N70-KW** is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

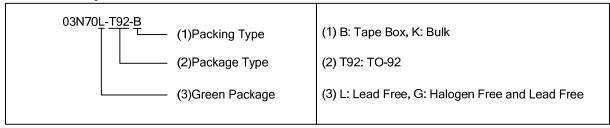


■ FEATURES

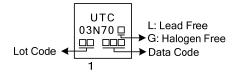
■ ORDERING INFORMATION

Ordering Number		Dackago	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
03N70L-T92-B	03N70G-T92-B	TO-92	G	D	S	Tape Box	
03N70L-T92-K	03N70G-T92-K	TO-92	G	D	S	Bulk	

Note: Pin Assignment: G: Gate D: Drain S: Source



■ MARKING



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^{*} $R_{DS(on)}$ < 28 Ω @ V_{GS} =10V, I_{D} =0.15A

^{*} High breakdown voltage

■ **ABSOLUTE MAXIMUM RATINGS** (T_C =25°C, unless otherwise specified)

PARAMETER		SYMBOL RATINGS		UNIT	
Drain-Source Voltage		V_{DSS}	700	V	
Gate-Source Voltage		V_{GSS}	±30	V	
Drain Current	Continuous	I _D	0.3	Α	
Diain Current	Pulsed	I _{DM}	1.2	Α	
Avalanche Current		I _{AR}	0.3	Α	
Power Dissipation		P_{D}	425	mW	
Junction Temperature Storage Temperature Range		TJ	150	°C	
		T _{STG}	-55 ~ + 150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	180	°C/W	
Junction to Case	θ _{JC}	38	°C/W	

■ ELECTRICAL CHARACTERISTICS (T_C =25°C, unless otherwise specified)

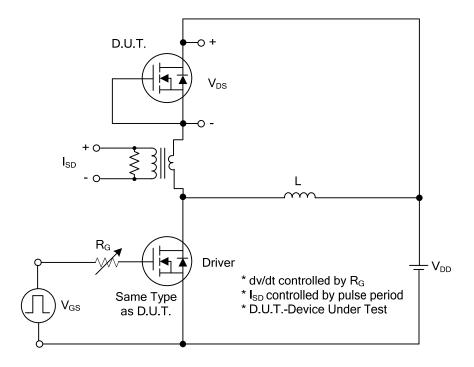
PARAMETER		SYMBOL	TEST CONDITIONS M		TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage	е	BV _{DSS}	I _D =250μA, V _{GS} =0V				V	
Drain-Source Leakage Current		I _{DSS}	V _{DS} =700V, V _{GS} =0V			10	μΑ	
Cata Source Leakage Current	Forward	- I _{GSS}	V_{GS} =+30V, V_{DS} =0V			+100	nΑ	
Gate-Source Leakage Current	Reverse		V _{GS} =-30V, V _{DS} =0V			-100	nA	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$			4.0	V	
Static Drain-Source On-State Re	sistance	R _{DS(ON)}	V_{GS} =10V, I_{D} =0.15A			28	Ω	
DYNAMIC PARAMETERS								
Input Capacitance	Capacitance C _{ISS}				68		pF	
Output Capacitance		Coss	V_{GS} =0V, V_{DS} =25V, f=1.0MHz		10.4		pF	
Reverse Transfer Capacitance		C_{RSS}			3.8		pF	
SWITCHING PARAMETERS								
Total Gate Charge		Q_G	V _{DS} = 50V, V _{GS} = 10V, I _D = 0.3A,		7.3		nC	
Gate to Source Charge		Q_{GS}	V_{DS} = 50V, V_{GS} = 10V, I_D = 0.3A, I_D =100 μ A (Note 1, 2)		0.6		nC	
Gate to Drain Charge		Q_GD	ID-100μA (Note 1, 2)		0.6		nC	
Turn-ON Delay Time		t _{D(ON)}	V_{DS} = 30V, V_{GS} = 10V, I_{D} = 0.3A,		31		ns	
Rise Time		t_R			12		ns	
Turn-OFF Delay Time		t _{D(OFF)}	$R_G = 25\Omega \text{ (Note 1, 2)}$		50		ns	
Fall-Time		t _F			11		ns	
SOURCE- DRAIN DIODE RATIN	NGS AND	CHARACTERI	STICS					
Maximum Body-Diode Continuou	us Current	Is				0.3	Α	
Maximum Body-Diode Pulsed Cu	urrent	I _{SM}				1.2	Α	
Drain-Source Diode Forward Vol	tage	V_{SD}	I _S =0.3A, V _{GS} =0V			1.4	V	

Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%.

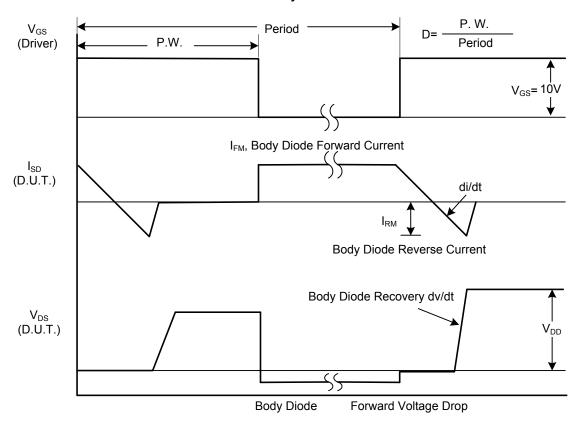
^{2.} Repetitive Rating: Pulse width limited by maximum junction temperature

^{2.} Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

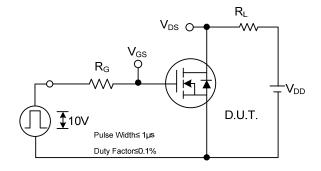


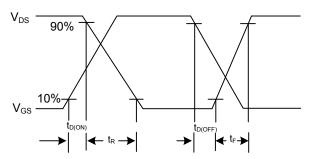
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

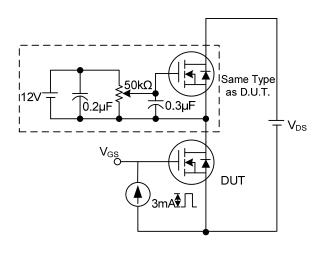
■ TEST CIRCUITS AND WAVEFORMS (Cont.)

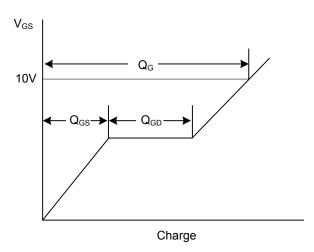




Switching Test Circuit

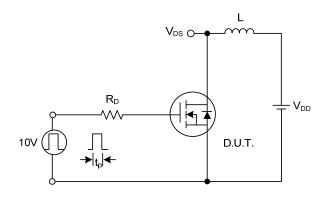
Switching Waveforms

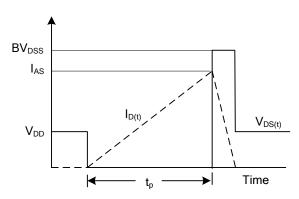




Gate Charge Test Circuit

Gate Charge Waveform





Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

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