



RAYSTAR

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RFY1230B-AWH-LNB

SPECIFICATION

General Specifications

- Size: 12.3 inch
- Pixel Number: 1920 RGB (H) x 720(V) pixel
- Module dimension: 364.0(H) x 175.0 (V) x 10.48 mm
- Active Area: 292.032(H) x 109.512(V) mm
- Pixel Pitch: 0.1521(H) x 0.1521 (V) mm
- LCD type: TFT, Normally Black, Transmissive
- Viewing Angle: 85/85/85/85
- Backlight Type: LED, Normally White
- TFT Interface: 2ch-LVDS
- PCAP IC: ILI2511 or equivalent
- PCAP Interface: USB (I2C available)
- PCAP FW Version: V6.0.0.0.0.0.3
- Touch Panel: Projected capacitive Touch Panel
- Surface: Glare

*Color tone slight changed by temperature and driving voltage.

Interface

1. TFT LCD MODULE

FPC connector is used for electronics interface.

AORORA F31L-1A7H1-21050 , 50PIN

Pin no.	Symbol	Function
1	GND	Ground
2	NC	No connector
3	VCC	Digital Power
4	VCC	Digital Power
5	GND	Panel Power
6	GND	Panel Power
7	NC	No connector
8	NC	No connector
9	GND	Ground
10	ORXIN0-	Odd pixel negative LVDS differential clock input
11	ORXIN0+	Odd pixel positive LVDS differential clock input
12	ORXIN1-	Odd pixel negative LVDS differential clock input
13	ORXIN1+	Odd pixel positive LVDS differential clock input
14	ORXIN2-	Odd pixel negative LVDS differential clock input
15	ORXIN2+	Odd pixel positive LVDS differential clock input
16	ORXCLKIN-	Odd pixel negative LVDS differential clock input
17	ORXCLKIN+	Odd pixel positive LVDS differential clock input
18	ORXIN3-	Odd pixel negative LVDS differential clock input
19	ORXIN3+	Odd pixel positive LVDS differential clock input
20	ERXIN0-	Even pixel negative LVDS differential clock input
21	ERXIN0+	Even pixel positive LVDS differential clock input
22	ERXIN1-	Even pixel negative LVDS differential clock input
23	ERXIN1+	Even pixel positive LVDS differential clock input
24	ERXIN2-	Even pixel negative LVDS differential clock input
25	ERXIN2+	Even pixel positive LVDS differential clock input
26	ERXCLKIN-	Even pixel negative LVDS differential clock input
27	ERXCLKIN+	Even pixel positive LVDS differential clock input
28	ERXIN3-	Even pixel negative LVDS differential clock input
29	ERXIN3+	Even pixel positive LVDS differential clock input
30	GND	Ground

31	NC	No connector
32	RESETB	Global reset pin, active low.
33	STBYB	Standby mode, active low.
34	CA3	Output signal to indicate self protection mode, when DE,HS,VS,DCLK, any of these signals is missing, it will become High. If using this pin, CA3 need to pulled low by an resistor,else , let it floating.
35	SCL	Serial interface clock input. (User folating)
36	SDA	Serial interface data input/output.(User folating)
37	CSB	Serial interface chip enable.(User folating)
38	GND	Power Ground
39	GND	Power Ground
40	NC	No connector
41	LEDA	LED power (Anode)
42	LEDA	LED power (Anode)
43	LEDA	LED power (Anode)
44	NC	No connector
45	LEDK1	Cathode 1
46	LEDK2	Cathode 2
47	LEDK3	Cathode 3
48	LEDK4	Cathode 4
49	NTC_A	NTC_Anode
50	NTC_K	NTC_Cathode

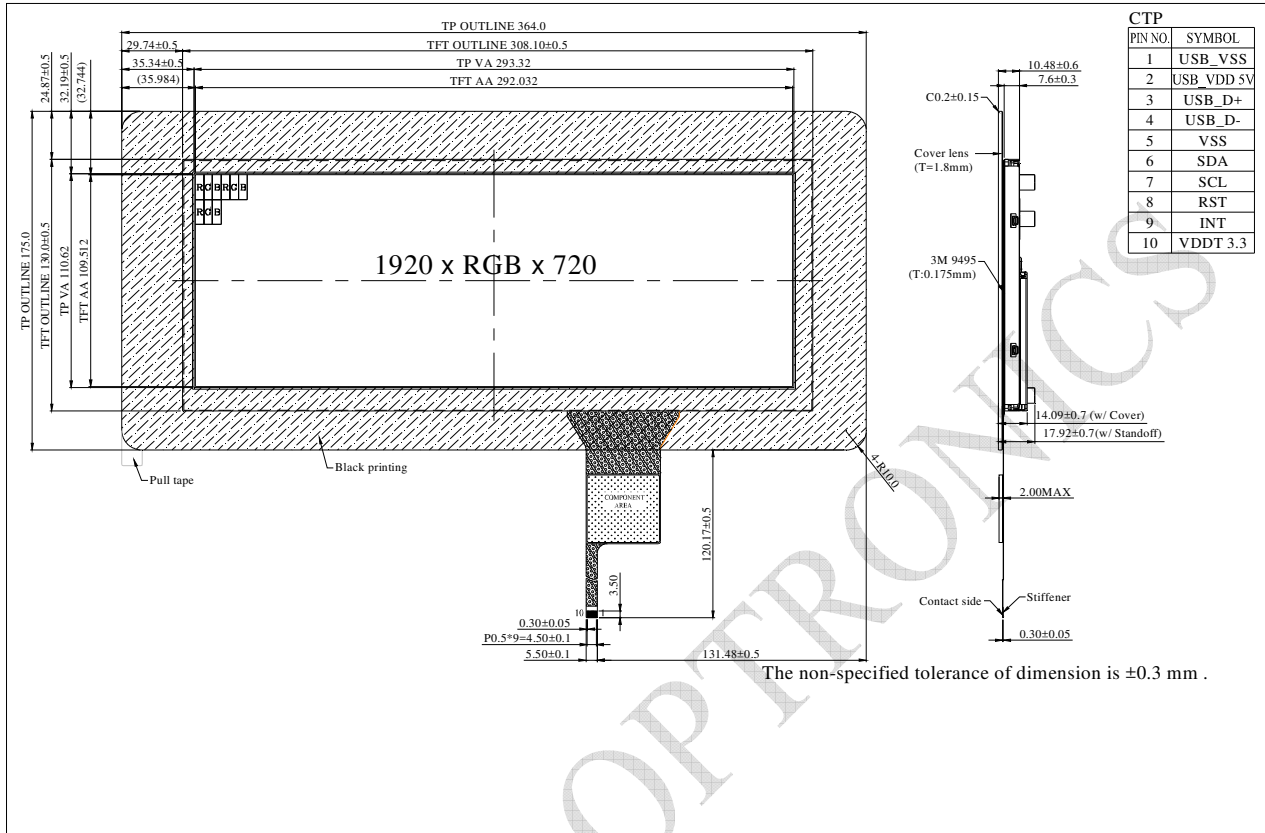
2. PCAP PIN Definition

Pin	Symbol	Function
1	USB_VSS	System ground
2	USB_VDD 5V	Power supply
3	USB_D+	Data +
4	USB_D-	Data -
5	VSS	System ground
6	SDA	I2C data input and output
7	SCL	I2C clock input
8	RST	External Reset, Low is active
9	INT	External interrupt to the host
10	VDDT 3.3	Power supply

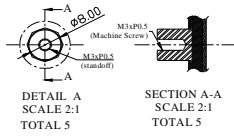
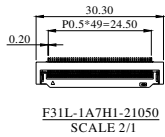
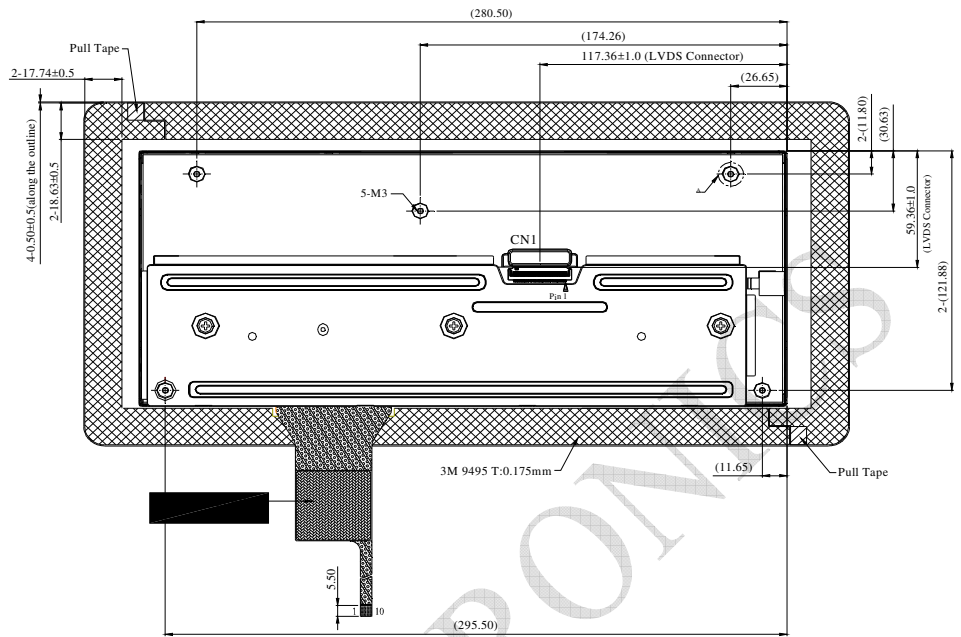
Note: Interface can support both USB and I2C,USB is main function

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Contour Drawing



CN1(F31L-1A7H1-21050)			
PIN NO	SYMBOL	PIN NO	SYMBOL
1	GND	26	ERXCLKIN-
2	NC	27	ERXCLKIN+
3	VCC	28	ERXIN3-
4	VCC	29	ERXIN3+
5	GND	30	GND
6	GND	31	NC
7	NC	32	RESET
8	NC	33	STBYB
9	GND	34	CA3
10	ORXIN0-	35	SCL
11	ORXIN0+	36	SDA
12	ORXIN1-	37	CSB
13	ORXIN1+	38	GND
14	ORXIN2-	39	GND
15	ORXIN2+	40	NC
16	ORXCLKIN-	41	LEDA
17	ORXCLKIN+	42	LEDA
18	ORXIN3-	43	LEDA
19	ORXIN3+	44	NC
20	ERXIN0-	45	LEDK1
21	ERXIN0+	46	LEDK2
22	ERXIN1-	47	LEDK3
23	ERXIN1+	48	LEDK4
24	ERXIN2-	49	NTC_A
25	ERXIN2+	50	NTC_K



The non-specified tolerance of dimension is ±0.3 mm .

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Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-30	—	+85	°C
Storage Temperature	TST	-40	—	+85	°C

Electrical Characteristics

TFT LCD Module

Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	VCC	3	3.3	3.6	V
Input signal voltage	VIH	VCC *0.7	-	VCC	V
	VIL	0	-	VCC *0.3	V
Current of power supply	ICC	-	-	500	mA
Inrush current	IRUSH	-	-	2.0	A
Supply CTP(USB)	USB_VDD 5V	4.75	5.0	5.25	V
	I USB_VDD	-	90	135	mA
Supply CTP(I2C)	VDDT 3.3	3.15	3.3	3.45	V
	I_VDDT	-	90	135	mA