RAYSTAR Optronics, Inc.
曜凌光電股份有限公司

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Raystar Optronics, Inc.
T: +886-4-2565-0761 | F: +886-4-2565-0760
sales@raystar-optronics.com | www.raystar-optronics.com

SPECIFICATION
Model No:
REA002004C

General Specification
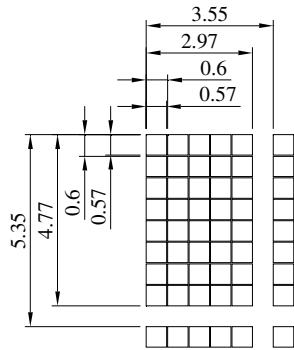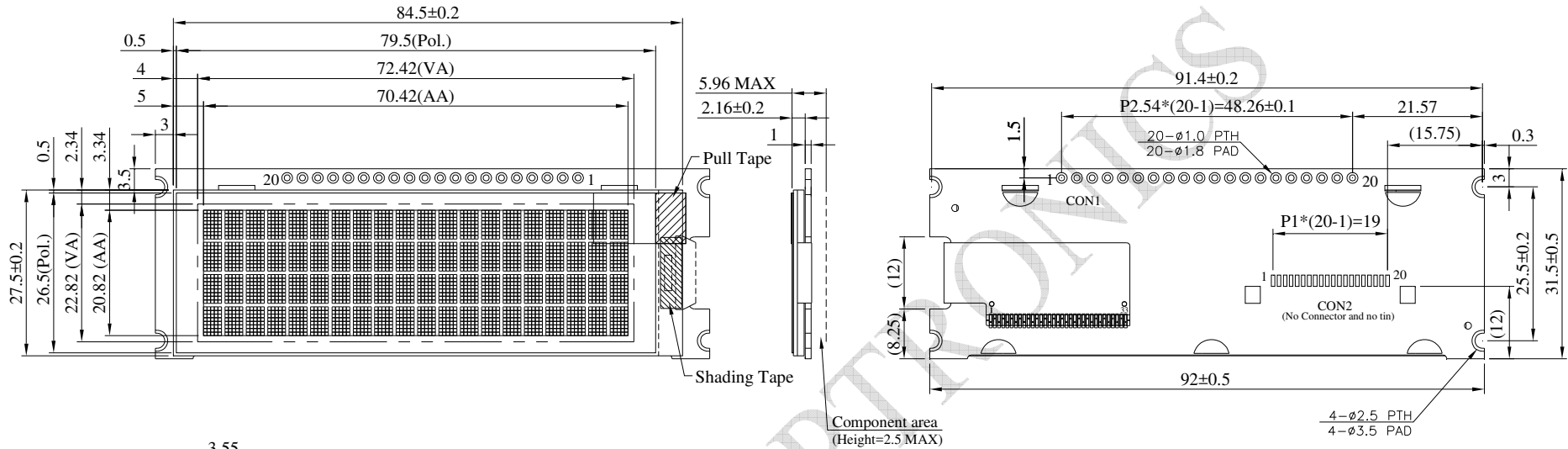
The Features is described as follow:

- Module dimension: 92.0 x 31.5 x 5.96 Max. mm

- View area: 72.42 x 22.82mm

- Active area: 70.42 x 20.82 mm

- Number of Characters: 20 characters x 4 Lines

- Dot size: 0.57 x 0.57 mm

- Dot pitch: 0.60 x 0.60 mm

- Character size: 2.97 x 4.77 mm

- Character pitch: 3.55 x 5.35 mm

- Duty: 1/32

- Emitting Color: OLED , Monochrome

- IC: SSD1311

- Interface: 6800,8080,SPI,I2C

- Size: 2.89 inch

Interface Pin Function

Pin No.	Symbol	Pin Type	Description
1	VSS	P	Ground
2	VDD	P	Power supply and power supply for interface logic level.
3	REGVDD	I	This pin is pulled LOW, internal VDD regulator is disabled (Low voltage I/O application).
4	D/C#	I	This pin is Data/Command control pin connecting to the MCU. When the pin is pulled HIGH, the data at D[7:0] will be interpreted as data. When the pin is pulled LOW, the data at D[7:0] will be transferred to a command register. In I2C mode, this pin acts as SA0 for slave address selection. When serial interface is selected, this pin must be connected to VSS.
5	R/W# (WR#)	I	This pin is read / write control input pin connecting to the MCU interface. When 6800 interface mode is selected, this pin will be used as Read/Write (R/W#) selection input. Read mode will be carried out when this pin is pulled HIGH and write mode when LOW. When 8080 interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled LOW and the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS.
6	E/RD#	I	This pin is MCU interface input. When 6800 interface mode is selected, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled HIGH and the chip is selected. When 8080 interface mode is selected, this pin receives the Read (RD#) signal. Read operation is initiated when this pin is pulled LOW and the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS.

7	D0		These pins are bi-directional data bus connecting to the MCU data bus.
8	D1		Unused pins are recommended to tie LOW.
9	D2		
10	D3	I/O	When serial interface mode is selected, D0 will be the serial clock input: SCLK; D1 will be the serial data input: SID and
11	D4		D2 will be the serial data output: SOD.
12	D5		
13	D6		When I2C mode is selected, D2, D1 should be tied together and serve as SDAout, SDAin in application and D0 is the
14	D7		serial clock input, SCL.

15	CS#	I	This pin is the chip select input connecting to the MCU. The chip is enabled for MCU communication only when CS# is pulled LOW (active LOW). In I2C mode, this pin must be connected to VSS.

16	RES#	I	This pin is reset signal input. When the pin is pulled LOW, initialization of the chip is executed. Keep this pin pull HIGH during normal operation.

17	BS0		setting as described in the following table. BS2, BS1 and BS0 are pin select. Bus Interface selection
18	BS1	I	
19	BS2		Note (1) 0 is connected to VSS (2) 1 is connected to VDD

For pins 17–19 (BS0, BS1, BS2):

BS[2:0]	Interface
000	Serial Interface
001	Invalid
010	I²C
011	Invalid
100	8-bit 6800 parallel
101	4-bit 6800 parallel
110	8-bit 8080 parallel
111	4-bit 8080 parallel

20	VSS (FRGnd)	P	Ground

Contour Drawing & Block Diagram

84.5±0.2
79.5(Pol.)
72.42(VA)
70.42(AA)
0.5
4
5
3
0.5
2.34
3.34
3.5
27.5±0.2
26.5(Pol.)
22.82 (VA)
20.82 (AA)

20 ○○○○○○○○○○○○○○○○○○○○ 1

Pull Tape

5.96 MAX
2.16±0.2
1

Shading Tape

Component area
(Height=2.5 MAX)

91.4±0.2
P2.54*(20-1)=48.26±0.1
21.57
1.5
20-⌀1.0 PTH
20-⌀1.8 PAD
(15.75)
0.3

1 ○○○○○○○○○○○○○○○○○○○○ 20
CON1

P1*(20-1)=19

1 □□□□□□□□□□□□□□□□□□□□ 20
CON2
(No Connector and no tin)

(12)
(8.25)

92±0.5

25.5±0.2
31.5±0.5
3
(12)

4-⌀2.5 PTH
4-⌀3.5 PAD

3.55
2.97
0.6
0.57
5.35
4.77
0.6
0.57

DOT SIZE
SCALE 5/1

S99 (SEG49)
⋮
S1 (SEG0)
C1 (COM0)
⋮
C32 (COM31)
S2 (SEG50)
⋮
S100 (SEG99)

100*32

PIN	SYMBOL	PIN	SYMBOL
1	VSS	11	D4
2	VDD	12	D5
3	REGVDD	13	D6
4	D/C#	14	D7
5	R/W#	15	CS#
6	E/RD#	16	RES#
7	D0	17	BS0
8	D1	18	BS1
9	D2	19	BS2
10	D3	20	VSS

The non-specified tolerance of dimension is ±0.3 mm .

Absolute Maximum Ratings

Item	Symbol	Min	Max	Unit
Supply Voltage For Logic	VDD-VSS	-0.3	3.6	V
Operating Temperature	TOP	-40	+80	°C
Storage Temperature	TST	-40	+85	°C

Electrical Characteristics

DC Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	VDD-VSS	—	2.8	3.0	3.3	V
Input High Volt.	VIH	—	0.8xVDD	—	VDD	V
Input Low Volt.	VIL	—	GND	—	0.2xVDD	V
Output High Volt.	VOH	IOH=-0.5mA	0.8xVDD	—	VDD	V
Output Low Volt.	VOL	IOL=0.5mA	GND	—	0.2xVDD	V
50% Check Board Operating Current	IDD	VDD=3V	-	60	110	mA