RAYSTAR

SPECIFICATION

OLED SPECIFICATION

Model No:

REX012864Q-CTP

General Specification

The Features is described as follow:

- Dot Matrix: 128 x 64

- Module dimension: 74.8 × 44.16 × 3.66 mm

- Active Area: 61.41 x 30.69 mm

- Pixel Size: 0.45 x 0.45 mm

- Pixel Pitch: 0.48 x 0.48 mm

- Display Mode: Passive Matrix

- Display Color: Monochrome (White)

- Drive Duty: 1/64 Duty

- OLED IC: SSD1309

- OLED Interface: 6800,8080,4-Wire SPI,I2C

- Size: 2.7 inch

- CTP IC: GT911

- Detect Point:1

- CTP Interface: I2C

- Surface: Normal Glare
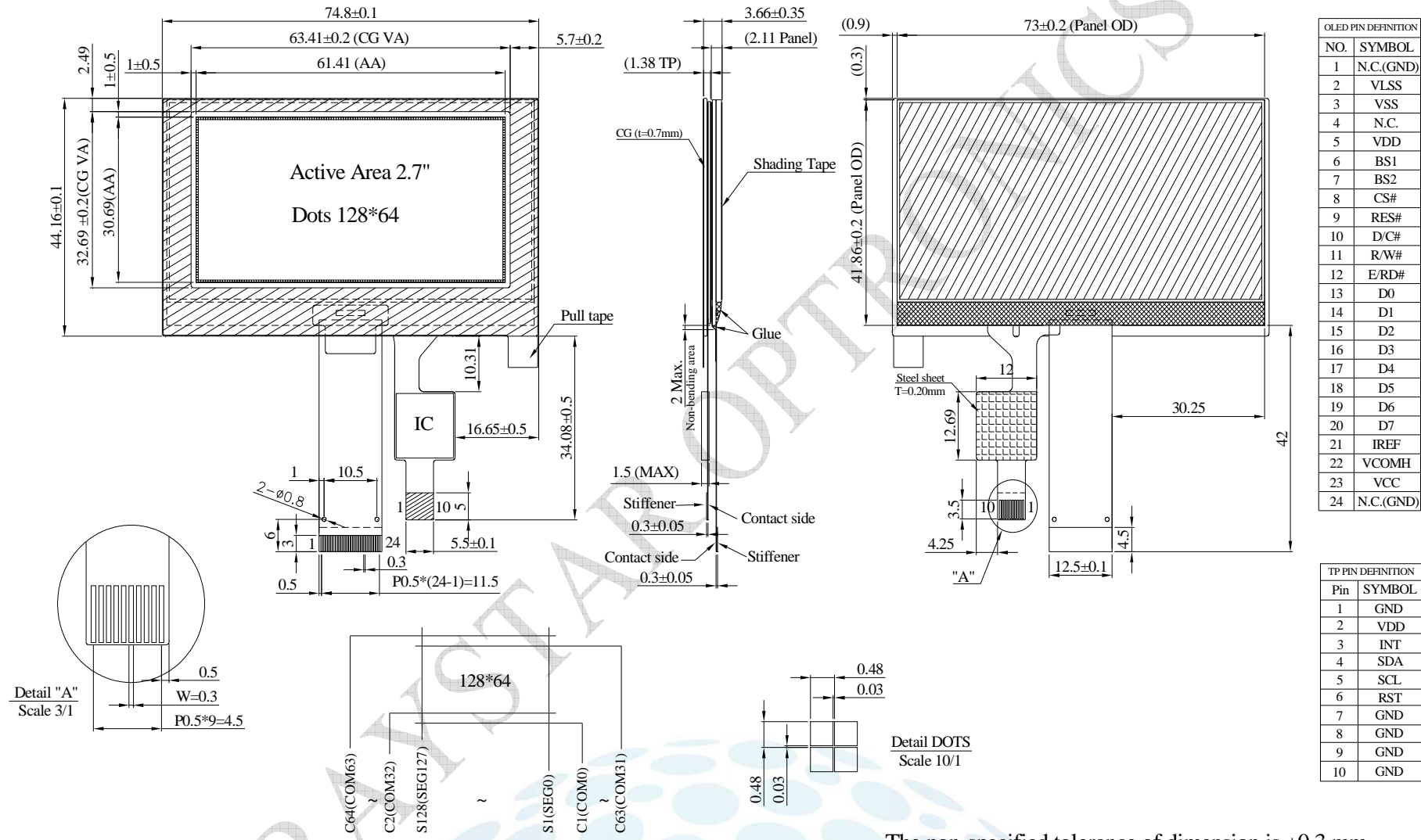
Interface Pin Function

No.	Symbol	Function
1	NC(GND)	No connection
2	VLSS	This is an analog ground pin
3	VSS	Ground.
4	NC	No connection
5	VDD	Power supply pin for core logic operation
6	BS1	MCU bus interface selection pins. Select appropriate logic setting as described in the following table. BS2 and BS1 are pin select |	BS1	BS2
I2C	1	0
4-wire Serial	0	0
8-bit 68XX Parallel	0	1
8-bit 80XX Parallel	1	1	
7	BS2	Note (1) 0 is connected to VSS (2) 1 is connected to VDD
8	CS#	This pin is the chip select input connecting to the MCU. The chip is enabled for MCU communication only when CS# is pulled LOW (active LOW).
9	RES#	This pin is reset signal input. When the pin is pulled LOW, initialization of the chip is executed. Keep this pin pull HIGH during normal operation.
10	D/C#	This pin is Data/Command control pin connecting to the MCU. When the pin is pulled HIGH, the data at D[7:0] will be interpreted as data. When the pin is pulled LOW, the data at D[7:0] will be transferred to a command register. In I2C mode, this pin acts as SA0 for slave address selection.
11	R/W#	This pin is read / write control input pin connecting to the MCU interface. When 6800 interface mode is selected, this pin will be used as Read/Write (R/W#) selection input. Read mode will be carried out when this pin is pulled HIGH and write mode when LOW. When 8080 interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled LOW and the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS.

12	E/RD#	This pin is MCU interface input. When 6800 interface mode is selected, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled HIGH and the chip is selected. When 8080 interface mode is selected, this pin receives the Read (RD#) signal. Read operation is initiated when this pin is pulled LOW and the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS.
13-20	D0~D7	These pins are bi-directional data bus connecting to the MCU data bus. Unused pins are recommended to tie LOW. When serial interface mode is selected, D0 will be the serial clock input: SCLK; D1 will be the serial data input: SDIN and D2 should be kept NC. When I2C mode is selected, D2, D1 should be tied together and serve as SDAout, SDAin in application and D0 is the serial clock input, SCL.
21	IREF	This pin is the segment output current reference pin. IREF is supplied externally.
22	VCOMH	COM signal deselected voltage level. A capacitor should be connected between this pin and VSS.
23	VCC	Power supply for panel driving voltage. This is also the most positive power voltage supply pin.
24	NC(GND)	No connection

CTP PIN Definition

No.	Symbol	Function
1	GND	Power ground
2	VDD	Power supply
3	INT	Interrupt signal, active low, asserted to request Host start a new transaction
4	SDA	I2C data signal
5	SCL	I2C clock signal
6	RST	External reset signal, active low
7	GND	Power ground
8	GND	Power ground
9	GND	Power ground
10	GND	Power ground

Contour Drawing & Block Diagram

Active Area 2.7"

Dots 128*64

74.8±0.1
63.41±0.2 (CG VA)
61.41 (AA)
5.7±0.2
2.49
1±0.5
1±0.5
44.16±0.1
32.69 ±0.2(CG VA)
30.69(AA)

Pull tape
10.31
IC
16.65±0.5
34.08±0.5
1 10 5
5.5±0.1
0.3
P0.5*(24-1)=11.5
2 - Ø0.8
6 3
1 24
1 10.5
0.5

Detail "A"
Scale 3/1
0.5
W=0.3
P0.5*9=4.5

3.66±0.35
(2.11 Panel)
(1.38 TP)
CG (t=0.7mm)
Shading Tape
Glue
2 Max.
Non-bending area
1.5 (MAX)
Stiffener
0.3±0.05
Contact side
0.3±0.05
Contact side
Stiffener

(0.9)
73±0.2 (Panel OD)
(0.3)
41.86±0.2 (Panel OD)
Steel sheet
T=0.20mm
12
12.69
30.25
42
3.5
10 1
4.25
12.5±0.1
4.5
"A"

128*64

C64(COM63)
~
C2(COM32)
S128(SEG127)
~
S1(SEG0)
C1(COM0)
~
C63(COM31)

0.48
0.03
0.48
0.03

Detail DOTS
Scale 10/1

OLED PIN DEFINITION	
NO.	SYMBOL
1	N.C.(GND)
2	VLSS
3	VSS
4	N.C.
5	VDD
6	BS1
7	BS2
8	CS#
9	RES#
10	D/C#
11	R/W#
12	E/RD#
13	D0
14	D1
15	D2
16	D3
17	D4
18	D5
19	D6
20	D7
21	IREF
22	VCOMH
23	VCC
24	N.C.(GND)

TP PIN DEFINITION	
Pin	SYMBOL
1	GND
2	VDD
3	INT
4	SDA
5	SCL
6	RST
7	GND
8	GND
9	GND
10	GND

The non-specified tolerance of dimension is ±0.3 mm .

Absolute Maximum Ratings

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage for Logic	VDD	-0.3	4	V
Supply Voltage for Display	VCC	0	15	V
Operating Temperature	TOP	-20	+70	°C
Storage Temperature	TSTG	-30	+80	°C

Touch Panel Controller GT911

Parameter	Symbol	Min	Max	Unit
Power Supply Voltage	VDD	2.66	3.47	V

Electrical Characteristics

DC Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage for Logic	VDD	—	2.8	3.0	3.3	V
Supply Voltage for Display	VCC	—	12.5	13	13.5	V
High Level Input	VIH	—	0.8×VDD	—	—	V
Low Level Input	VIL	—	—	—	0.2×VDD	V
High Level Output	VOH	—	0.9×VDD	—	—	V
Low Level Output	VOL	—	—	—	0.1×VDD	V
50% Check Board operating Current		VCC =13.0V	—	30	50	mA

Touch Panel Controller GT911

Item	Symbol	Min	Typ	Max	Unit
Supply Voltage	VDD	2.8	3.0	3.3	V
Input High Volt.	VIH	0.75xVDD		VDD+0.3	V
Input Low Volt.	VIL	-0.3	—	0.25xVDD	V
Output High Volt.	VOH	0.85xVDD	—	—	V
Output Low Volt.	VOL	—	—	0.15xVDD	V