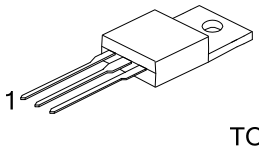
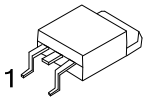
UTT18P10

Power MOSFET

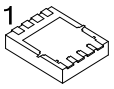
-100V, -18A P-CHANNEL POWER MOSFET



TO-220



TO-252



DFN5060-8

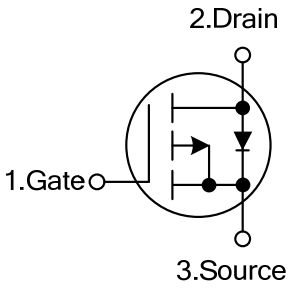
■ DESCRIPTION

The UTC **UTT18P10** is a P-channel power MOSFET using UTC's advanced technology to provide the customers with high switching speed, cost-effectiveness and a minimum on-state resistance. It can also withstand high energy in the avalanche.

■ FEATURES

* $R_{DS(ON)}$ ≤ 180 mΩ @ V_{GS}=-10V, I_D=-9.0A
  $R_{DS(ON)}$ ≤ 210 mΩ @ V_{GS}=-4.5V, I_D=-9.0A
* High Switching Speed

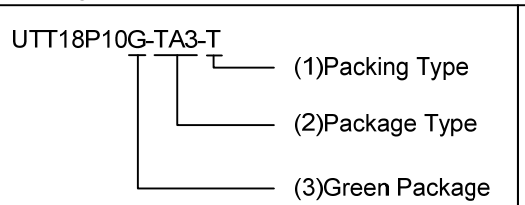
■ SYMBOL



■ ORDERING INFORMATION

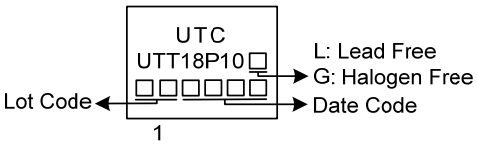
Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UTT18P10L-TA3-T	UTT18P10G-TA3-T	TO-220	G	D	S	-	-	-	-	-	Tube
UTT18P10L-TN3-R	UTT18P10G-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
UTT18P10L-K08-5060-R	UTT18P10G-K08-5060-R	DFN5060-8	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source



(1) T: Tube, R: Tape Reel
(2) TA3: TO-220, TN3: TO-252
K08-5060: DFN5060-8
(3) G: Halogen Free and Lead Free, L: Lead Free

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	-100	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current	Continuous, V_{GSS}@-10V T_C=25°C	I_D	-18	A
	Pulsed (Note 3)	I_{DM}	-36	A
Avalanche Energy	Single Pulsed (Note 4)	E_{AS}	36.4	mJ
Power Dissipation (T_C=25°C)	TO-220	P_D	100	W
	TO-252		48	W
	DFN5060-8		13	W
Junction Temperature		T_J	+150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2.Repetitive Rating: Pulse width limited by maximum junction temperature.
3. L = 0.1mH, I_{AS} = -27A, V_{DD} = -50V, R_G = 25Ω, Starting T_J = 25°C
4. I_{SD} ≤ -18A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220	$θ_{JA}$	62.5	°C/W
	TO-252		110	
	DFN5060-8		65 (Note)	
Junction to Case	TO-220	$θ_{JC}$	1.25	°C/W
	TO-252		2.6 (Note)	
	DFN5060-8		9.6 (Note)	

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV_{DSS}	I_D=-250μA, V_{GS}=0V	-100			V
Drain-Source Leakage Current		I_{DSS}	V_{DS}=-100V, V_{GS}=0V,			-1	μA
Gate- Source Leakage Current	Forward	I_{GSS}	V_{GS}=+20V, V_{GS}=0V,			+100	nA
	Reverse		V_{GS}=-20V, V_{GS}=0V,			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=-250μA	-1.0		-3.0	V
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	V_{GS}=-10V, I_D=-9.0A			180	mΩ
			V_{GS}=-4.5V, I_D=-9.0A			210	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C_{ISS}	V_{DS}=-25V, V_{GS}=0V, f=1.0MHz		1487		pF
Output Capacitance		C_{OSS}			82.8		pF
Reverse Transfer Capacitance		C_{RSS}			70.6		pF
SWITCHING PARAMETERS							
Total Gate Charge		Q_G	V_{DS}=-80V, V_{GS}=-10V, I_D=-18A I_G=-1mA (Note 1, 2)		34.2		nC
Gate to Source Charge		Q_{GS}			9.5		nC
Gate to Drain ("Miller") Charge		Q_{GD}			5.2		nC
Turn-ON Delay Time		$t_{D(ON)}$	V_{DD}=-50V, V_{GS}=-10V, I_D=-18A, R_G=6Ω (Note 1, 2)		5.8		ns
Rise Time		t_R			17.3		ns
Turn-OFF Delay Time		$t_{D(OFF)}$			35.7		ns
Fall-Time		t_F			20.5		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I_S				-18	A
Maximum Body-Diode Pulsed Current (Note 2)		I_{SM}				-36	A
Drain-Source Diode Forward Voltage		V_{SD}	I_S=-18A, V_{GS}=0V			-5.0	V
Body Diode Reverse Recovery Time		t_{rr}	I_S=-18A, V_{GS}=0V, dI_F/dt=100A/μs (Note 2)		127.3		ns
Body Diode Reverse Recovery Charge		Q_{rr}			1.1		μC

Notes: 1. Essentially independent of operating temperature.
2. Pulse width ≤ 300μs; duty cycle ≤ 2%.

■ TEST CIRCUITS AND WAVEFORMS

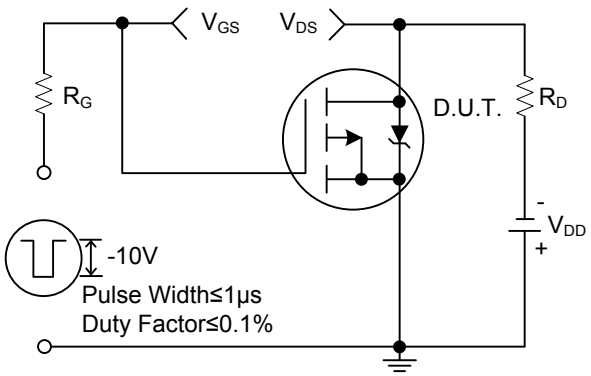


Fig. 1a Switching Time Test Circuit

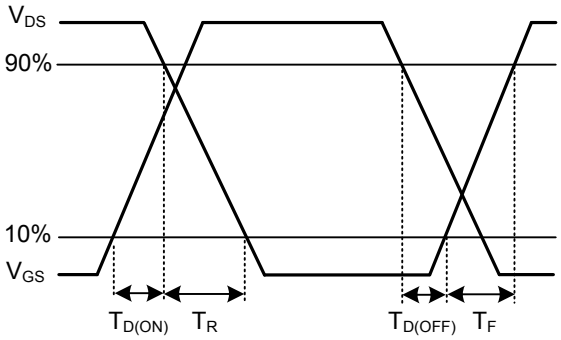


Fig. 1b Switching Time Waveforms

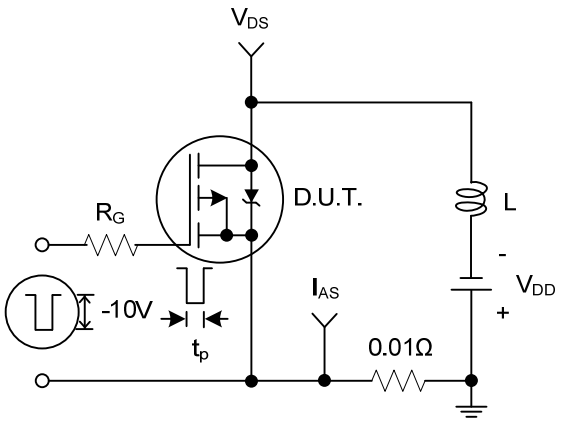


Fig. 2a Unclampled Inductive Test Circuit

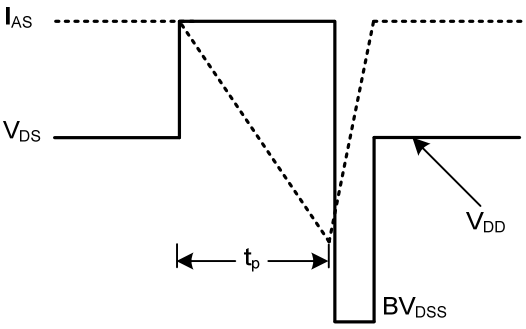


Fig. 2b Unclampled Inductive Waveforms

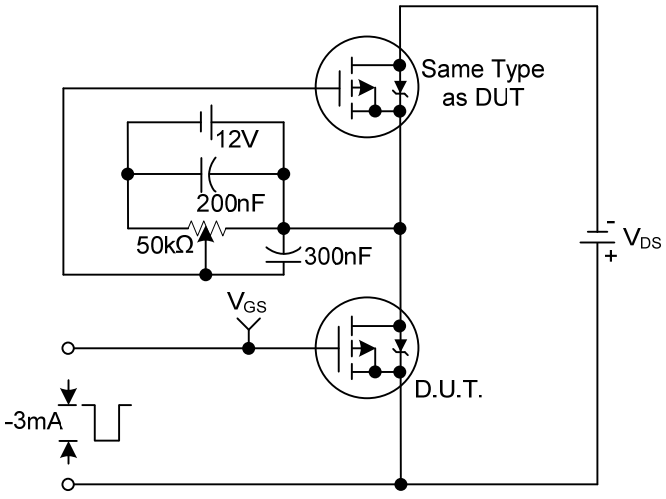


Fig.3a Gate Charge Test Circuit

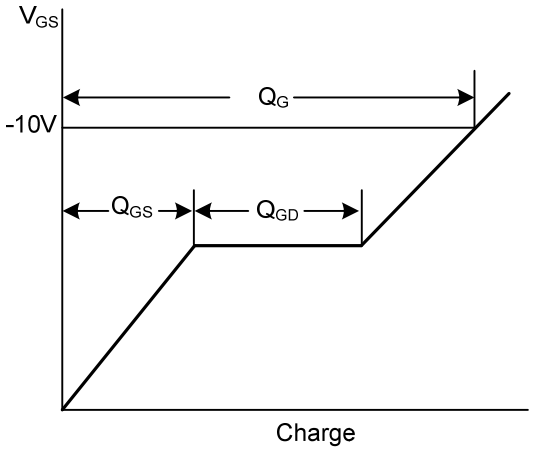
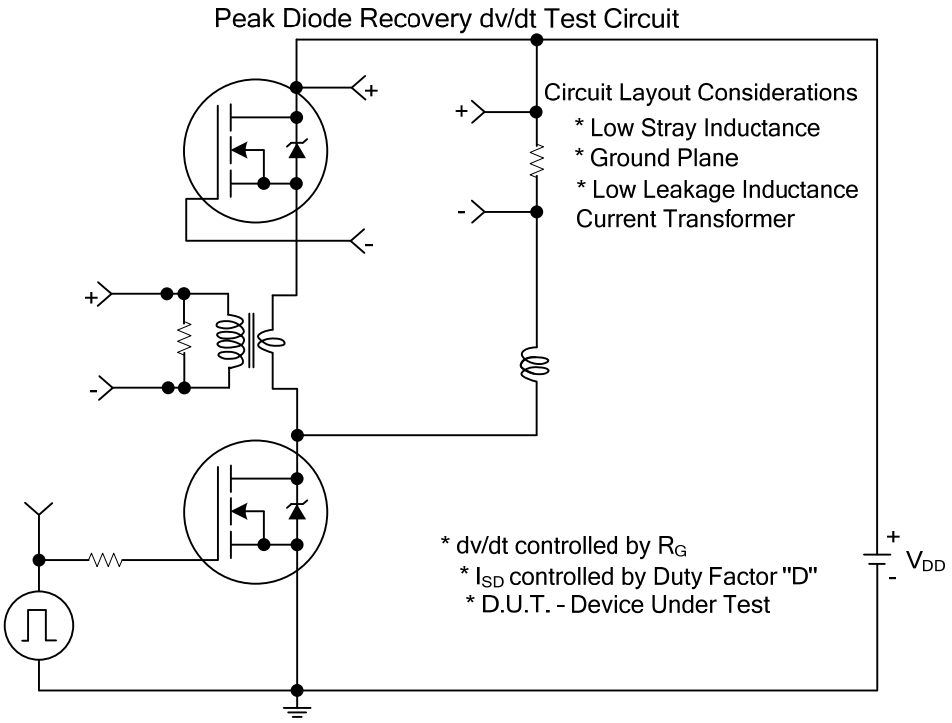
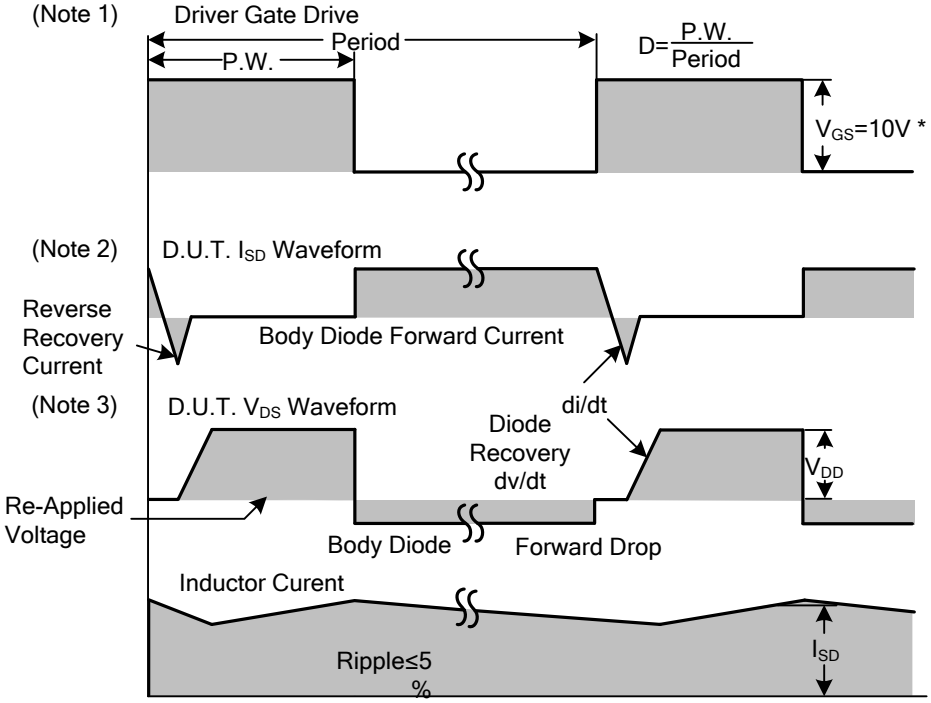


Fig. 3b Gate Charge Waveform

■ TEST CIRCUITS AND WAVEFORMS



* Reverse Polarity for P-Channel
** Use P-Channel Driver for P-Channel Measurements



*** V_{GS}=5V for Logic Level and 3V Drive Devices

For N and P Channel Power MOSFET

Notes: 1. Repetitive rating; pulse width limited by max. junction temperature.
2. V_{DD}=-25V, starting T_J=25°C, L=2.7mH, R_G=25Ω, I_{AS}=-18A. (See Figure 2)
3. I_{SD}≤-18A, di/dt≤200A/μs, V_{DD}≤BV_{DSS}, T_J≤150°C