

P-channel MOS Field Effect Transistor for Impedance Converter of Microphone

■ GENERAL DESCRIPTION

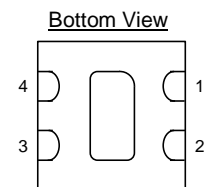
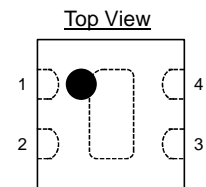
The **NJD2201** is a P-channel MOSFET for Impedance converter of microphone.

The **NJD2201** is the most suitable for the ECM especially which requires high SNR.

■ FEATURES

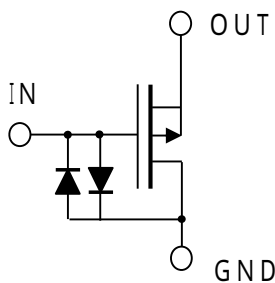
- | | |
|---------------------------|---------------------------------|
| • Supply Voltage | +1.0 to +10V at $R_L=15k\Omega$ |
| • Low Consumption Current | 85 μ A typ. |
| • Voltage Gain | -4dB typ. at $C_{IN}=3pF$ |
| • Low Output Noise | -115dBV typ. |
| • THD | 0.1% typ. |
| • Package Outline | DFN4-E1 (ESON4-E1) |

■ PACKAGE OUTLINE



NJD2201KE1

■ BLOCK DIAGRAM



■ PIN CONFIGURATION

1. GND
2. OUT
3. (NC)
4. IN

■ ABSOLUTE MAXIMUM RATING (Ta=25°C)

PARAMETER	SYMBOL	RATING	UNIT
Input Voltage (IN-GND)	V _{IN}	-0.8 to +0.8	V
Input Current (IN-GND)	I _{IN}	0.5	mA
Output Voltage (OUT-GND)	V _{OUT}	-0.5 to +6	V
Output Current (OUT-GND)	I _{OUT}	17	mA
Power Dissipation	P _D	370 * ¹⁾	mW
Operating Temperature Range	Topr	-40 to +105	°C
Storage Temperature Range	Tstg	-40 to +150	°C

*¹⁾ EIA/JEDEC STANDARD Test board (101.5x114.5x1.6mm, 2layer, FR-4) mounting. The PAD connecting to GND in the center part on the back

■ RECOMMENDED OPERATING VOLTAGE RANGE (Ta=25°C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage Range	V _{DD}	R _L =15kΩ	1	2	10	V

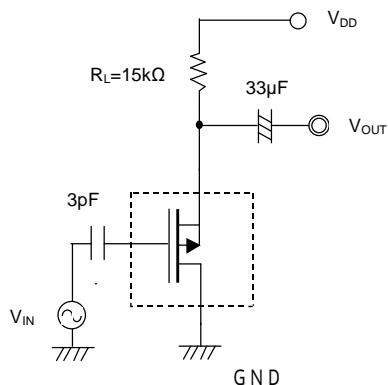
■ ELECTRICAL CHARACTERISTICS

(Ta=25°C, V_{DD}=2V, C_{IN}=3pF, R_L=15kΩ, f=1kHz, V_{IN}=10mVrms unless otherwise specified)

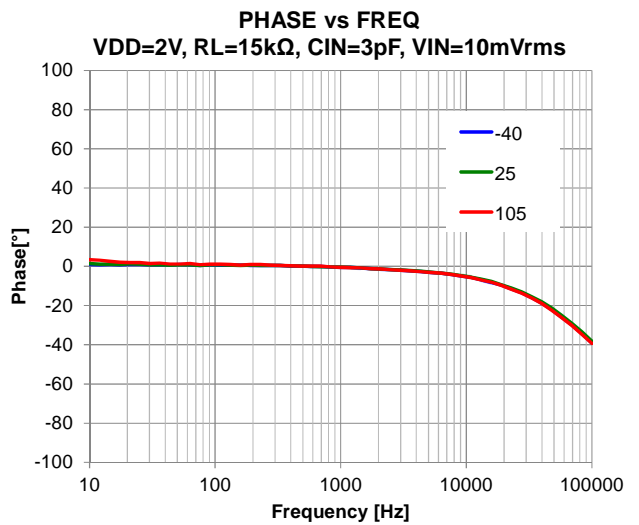
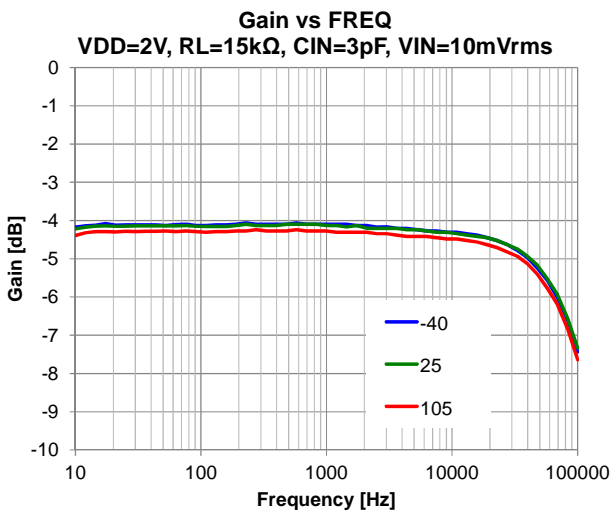
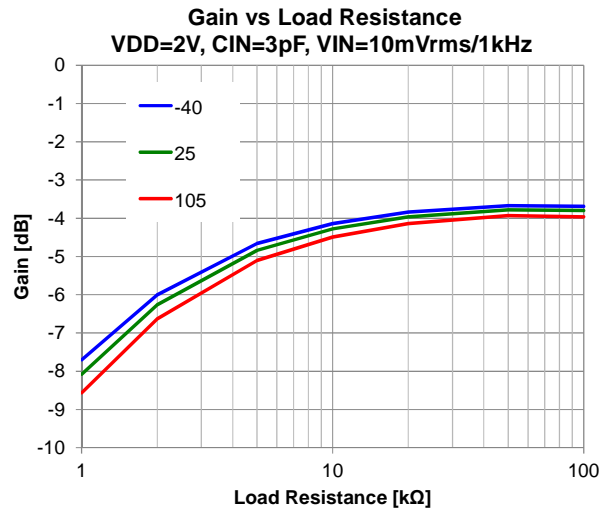
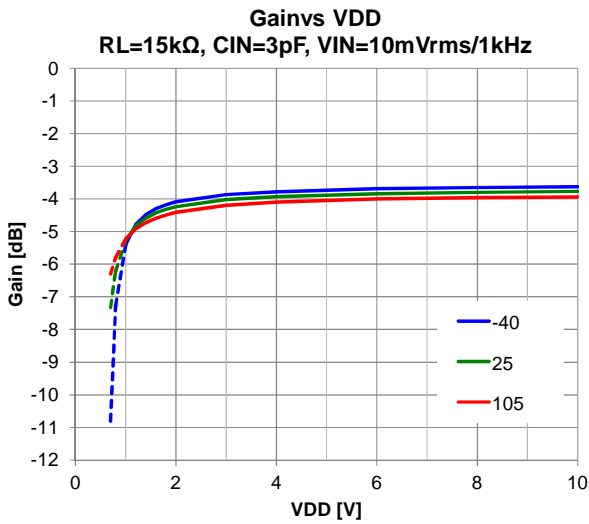
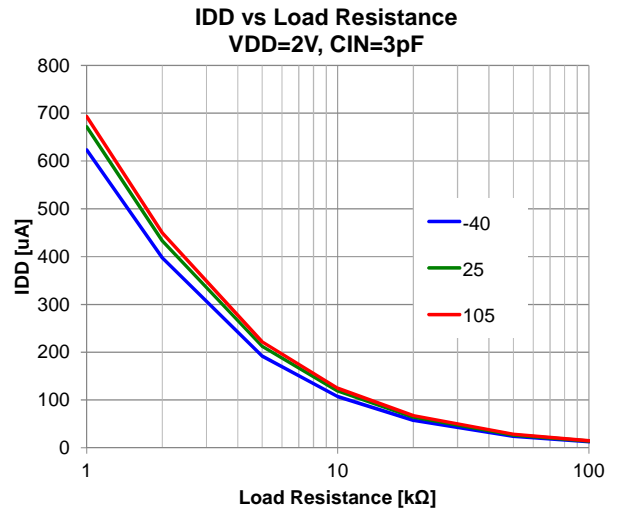
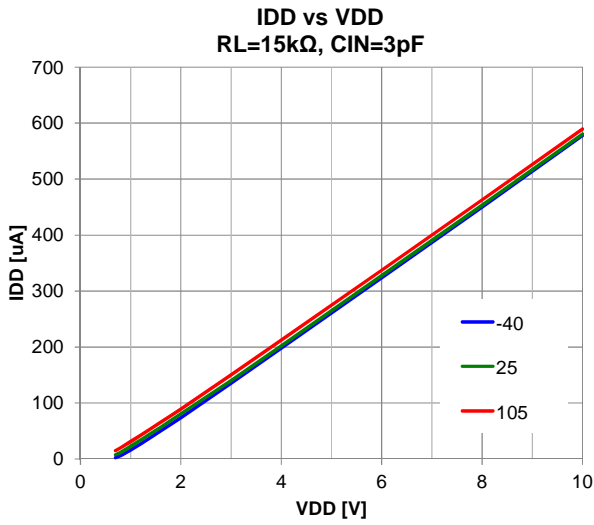
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Consumption Current	I _{DD}	V _{IN} =0V, C _{IN} =none	60	85	105	μA
Input Capacitance	C _{ISS}	f=1MHz, C _{IN} =none	-	1.5	-	pF
Voltage Gain	G _v		-5.5	-4.0	-	dB
Reduced Voltage Characteristics	ΔG _v	V _{DD} =2 to 1.5V	-	0.3	-	dB
Frequency Characteristics	ΔG _v f	f=1kHz to 110Hz	-	0.05	-	dB
Output Noise Voltage	N _v	V _{IN} =0Vrms, A-weight	-	-115	-	dBV
Total Harmonic Distortion	THD	V _{OUT} =30mVrms	-	0.1	-	%

■ TEST CIRCUIT

- ◆ Voltage Gain, Reduced Voltage Characteristics, Frequency Characteristics, Output Noise Voltage, Total Harmonic Distortion



■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS

